

28 1000 volts class power transistor modules

- Terminals layout in which drive wiring and power wiring do not come across.
- Suited for motor control applications with 380 to 440V volts inputs.

Device type	V _{CEO}	V _{CE0}	V _{CEX}	I _c	P _c	h _{FE} min.	I _c		Switching time (Max.)			Package	Net mass Grams	Equivalent circuit Page 32, 33
	Volts	Volts	Volts	cont. Amps.	Watts		I _c	V _{CE}	t _{on}	t _{stg}	t _f			
							Amps.	Volts	μsec.	μsec.	μsec.			
2DI30D-100	1000	1000	1000	30	300	100	30	5	2.5	15	3	M204	175	Fig. E1
2DI50D-100	1000	1000	1000	50	400	100	50	5	2.5	15	3	M204	175	Fig. E1
2DI75D-100	1000	1000	1000	75	500	100	75	5	2.5	15	3	M206	265	Fig. E1
2DI100D-100	1000	1000	1000	100	800	100	100	5	2.5	15	3	M207	550	Fig. E2
2DI150D-100	1000	1000	1000	150	1000	100	150	5	2.5	15	3	M207	550	Fig. E2
2DI200D-100	1000	1000	1000	200	1600	100	200	5	2.5	15	3	M207	550	Fig. E2
1DI300D-100	1000	1000	1000	300	2000	100	300	5	2.5	15	3	M105	560	Fig. D1
1DI400D-100	1000	1000	1000	400	2400	70	400	5	2.5	15	3	M105	560	Fig. D1

29 1200 volts class power transistor modules

- Power transistors and free wheel diodes are built into one package.
- Suited for motor control applications with 440 to 480 V AC inputs and power supplies.
- Terminal layout in which drive wiring and power wiring do not come across.

Device type	V _{CEO}	V _{CE0}	V _{CEX}	I _c	P _c	h _{FE} min.	I _c		Switching time (Max.)			Package	Net mass Grams	Equivalent circuit Page 32, 33
	Volts	Volts	Volts	cont. Amps.	Watts		I _c	V _{CE}	t _{on}	t _{stg}	t _f			
							Amps.	Volts	μsec.	μsec.	μsec.			
2DI30A-120	1200	1200	1200	30	300	70	30	5	3.0	15	3	M204	175	Fig. E1
2DI50A-120	1200	1200	1200	50	400	70	50	5	3.0	15	3	M206	265	Fig. E1
2DI100A-120	1200	1200	1200	100	800	70	100	5	3.0	15	3	M207	550	Fig. E2
2DI150A-120	1200	1200	1200	150	1000	70	150	5	3.0	15	3	M207	550	Fig. E2
1DI200A-120	1200	1200	1200	200	1400	70	200	5	2.5	15	3	M105	560	Fig. D1
1DI300A-120	1200	1200	1200	300	2000	70	300	5	3.0	15	3	M105	560	Fig. D1
1DI400A-120	1200	1200	1200	400	3120	100	400	5	3.0	15	3	M107	830	Fig. D1