

SPECIFICATION

Device Name : IGBT MODULE

(RoHS compliant product)

Type Name : 2MBI300U4N-120-50

Spec. No. : MS5F 6509

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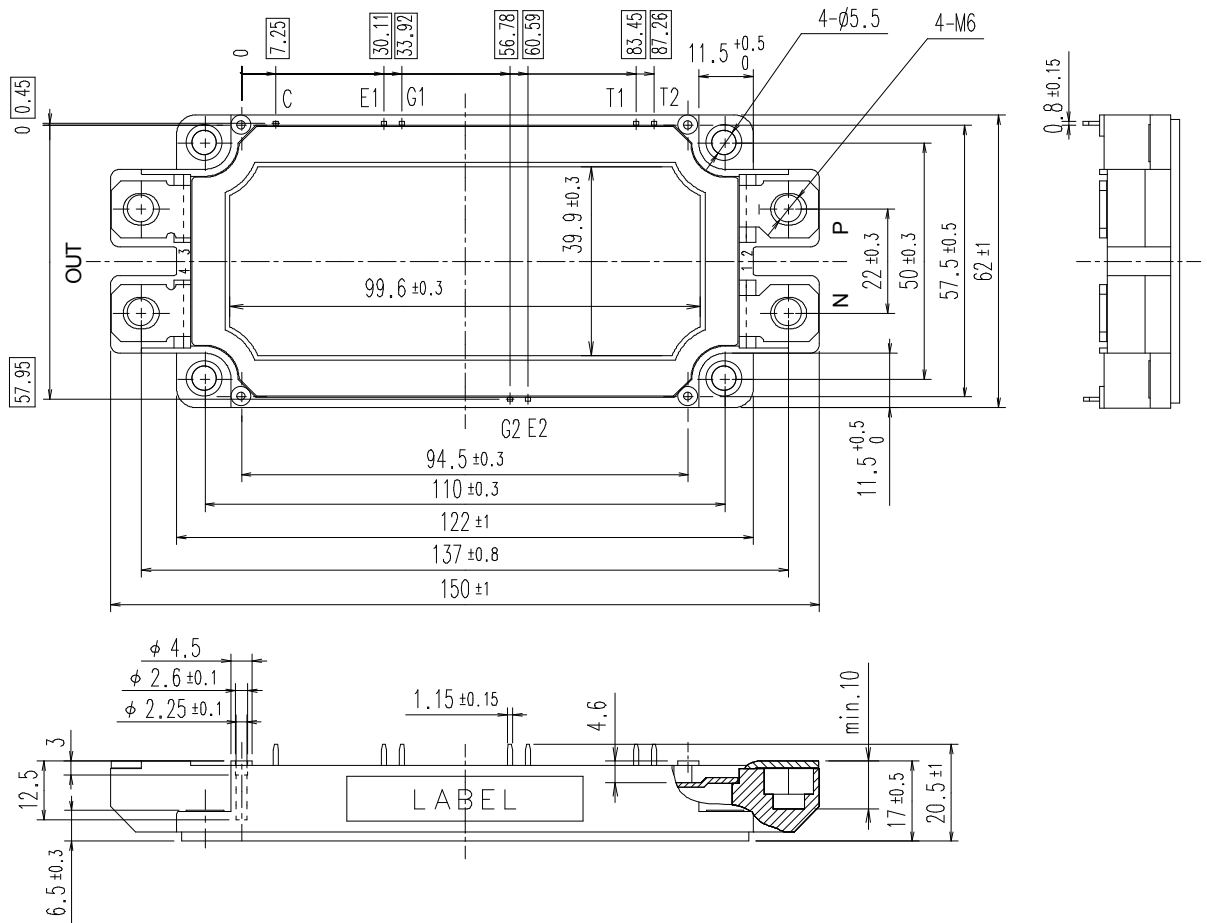
	DATE	NAME	APPROVED	Fuji Electric Device Technology Co., Ltd.		
DRAWN	Feb. -14 -06	H.Kaneda	T.Miyasaka	DWG.NO.	MS5F6509	1 / 14
CHECKED	Feb. -14 -06	M.Watanabe				a
CHECKED	- -	K.Yamada				

Revised Records

Date	Classi- fication	Ind.	Content	Applied date	Drawn	Checked	Checked	Approved
Feb. -14 -'06	Enactment	—	—————	Issued date	—	M.Watanabe	K.Yamada	T.Miyasaka
Sep. -20 -'07	Revision	a	Revised Outline Drawing (P3/14)		T.Koga	S.Miyashita	K.Yamada	T.Miyasaka

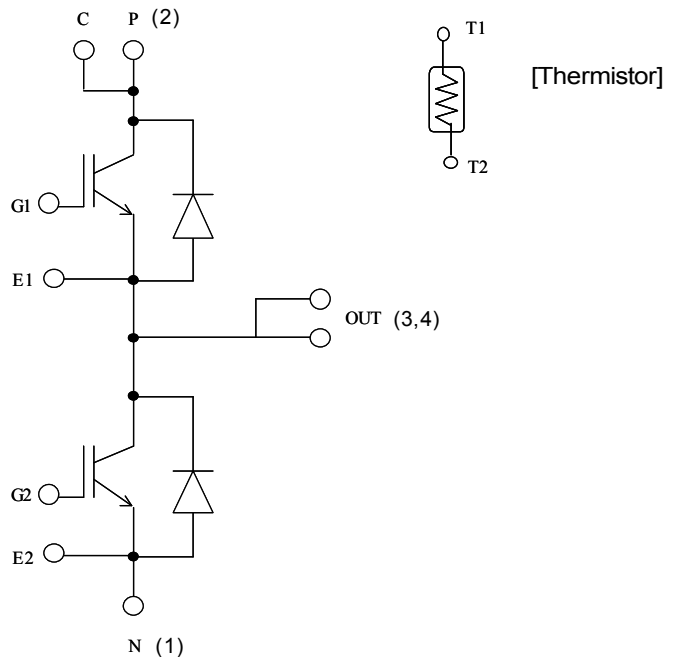
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1. Outline Drawing (Unit : mm) ㊦



NOTE) shows theoretical demension and tolerance is $\phi \pm 0.5$

2. Equivalent circuit



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3. Absolute Maximum Ratings (at Tc= 25°C unless otherwise specified)

Items		Symbols	Conditions	Maximum Ratings	Units
Collector-Emitter voltage		VCES		1200	V
Gate-Emitter voltage		VGES		±20	V
Collector current	Ic	Continuous	Tc=25°C	450	A
			Tc=80°C	300	
	Icp	1ms	Tc=25°C	900	
			Tc=80°C	600	
	-Ic			300	
-Ic pulse	1ms		600		
Collector Power Dissipation		Pc	1 device	1385	W
Junction temperature		Tj		+150	°C
Storage temperature		Tstg		-40 to +125	
Isolation voltage	between terminal and copper base (*1)	Viso	AC : 1min.	2500	VAC
	between thermistor and others (*2)				
Screw Mounting (*3)				3.5	N m
Torque Terminals (*4)				4.5	

(*1) All terminals should be connected together when isolation test will be done.

(*2) Two thermistor terminals should be connected together, each other terminals should be connected together and shorted to base plate when isolation test will be done.

(*3) Recommendable Value : 2.5 to 3.5 Nm (M5)

(*4) Recommendable Value : 3.5 to 4.5 Nm (M6)

4. Electrical characteristics (at Tj= 25°C unless otherwise specified)

Items	Symbols	Conditions	Characteristics			Units		
			min.	typ.	max.			
Inverter	Zero gate voltage collector current	ICES	VCE=1200V VGE=0V	-	-	3.0	mA	
	Gate-Emitter leakage current	IGES	VCE=0V VGE=±20V	-	-	600	nA	
	Gate-Emitter threshold voltage	VGE(th)	VCE=20V Ic=300mA	4.5	6.5	8.5	V	
	Collector-Emitter saturation voltage	VCE(sat) (terminal)	Ic=300A VGE=15V	Tj=25°C	-	2.30	2.45	V
				Tj=125°C	-	2.50	-	
		VCE(sat) (chip)		Tj=25°C	-	1.90	2.05	
				Tj=125°C	-	2.10	-	
	Input capacitance	Cies	VCE=10V, VGE=0V, f=1MHz	-	34	-	nF	
	Turn-on time	ton	Vcc=600V	-	0.32	1.20	us	
		tr	Ic=300A	-	0.10	0.60		
		tr(i)	VGE=±15V	-	0.03	-		
	Turn-off time	toff	RG=2.0Ω	-	0.41	1.00	us	
		tf		-	0.07	0.30		
	Forward on voltage	VF (terminal)	IF=300A VGE=0V	Tj=25°C	-	2.00	2.15	V
				Tj=125°C	-	2.10	-	
VF (chip)		Tj=25°C		-	1.65	1.80		
		Tj=125°C		-	1.75	-		
Reverse recovery time	trr	IF=300A	-	-	0.35	us		
Lead resistance, terminal-chip (*5)	R lead		-	1.00	-	mΩ		
Thermistor Resistance	R	T=25°C	-	5000	-	Ω		
		T=100°C	465	495	520			
B value	B	T=25/50°C	3305	3375	3450	K		

(*5) Biggest internal terminal resistance among arm.

5. Thermal resistance characteristics

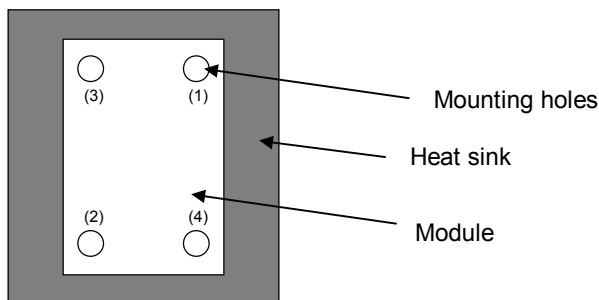
Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance(1device)	Rth(j-c)	IGBT	-	-	0.09	°C/W
		FWD	-	-	0.15	
Contact Thermal resistance (1 device) (*6)	Rth(c-f)	with Thermal Compound	-	0.0167	-	

(*6) This is the value which is defined mounting on the additional cooling fin with thermal compound.

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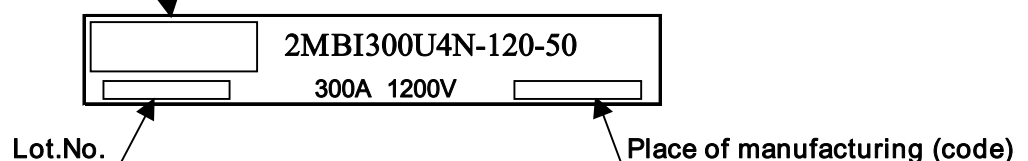
6. Recommend way of module mounting to Heat sink Clamping

- (1) Initial : 1/3 specified torque, sequence (1)→(2)→(3)→(4)
- (2) Final : Full specified torque (3.5 Nm), sequence(4)→(3)→(2)→(1)



7. Indication on module

Logo of production



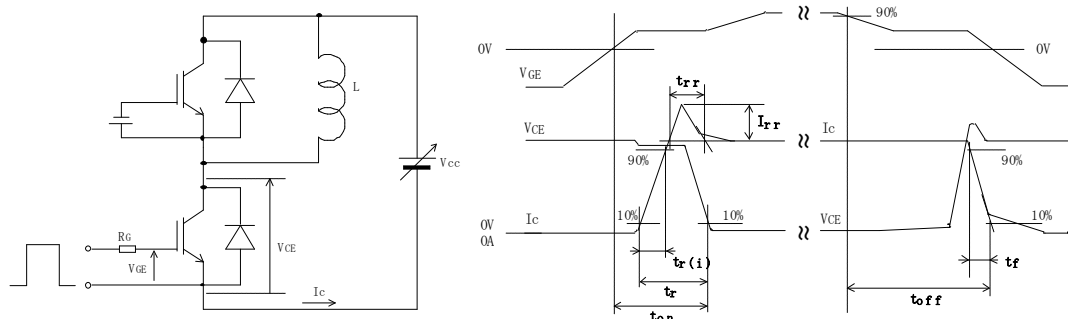
8. Applicable category

This specification is applied to IGBT-Module named 2MBI300U4N-120-50.

9. Storage and transportation notes

- The module should be stored at a standard temperature of 5 to 35°C and humidity of 45 to 75% . Be careful to solderability of the terminals if the module has passed over one year from manufacturing date, under the above storage condition.
- Store modules in a place with few temperature changes in order to avoid condensation on the module surface.
- Avoid exposure to corrosive gases and dust.
- Avoid excessive external force on the module.
- Store modules with unprocessed terminals.
- Do not drop or otherwise shock the modules when transporting.

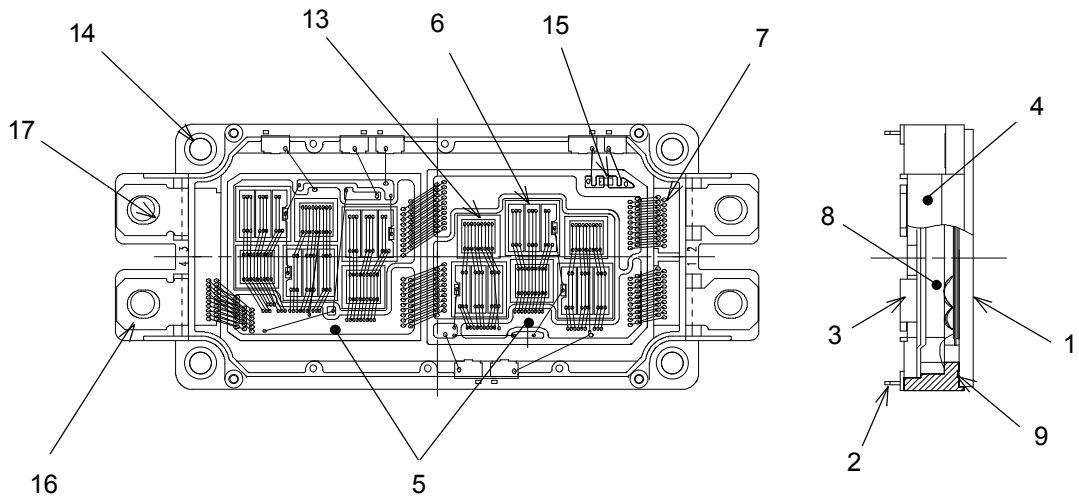
10. Definitions of switching time



11. Packing and Labeling

- Display on the packing box
- Logo of production
- Type name
- Lot No
- Products quantity in a packing box

12. List of material (材料リスト)



.....W.....W.W.....W.....W.....

13. RoHS Directive Compliance (RoHS 指令適用について)

料 適用

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14. Reliability test results

Reliability Test Items

Test categories	Test items	Test methods and conditions	Reference norms EIAJ ED-4701 (Aug.-2001 edition)	Number of sample	Acceptance number
Mechanical Tests	1 Terminal Strength (Pull test)	Pull force : 20N (Control terminal) 40N (Main terminal) Test time : 10±1 sec.	Test Method 401 Method>□	5	(0 : 1)
	2 Mounting Strength	Screw torque : 2.5 ~ 3.5 N·m (M5) 3.5 ~ 4.5 N·m (M6) Test time : 10±1 sec.	Test Method 402 method>□	5	(0 : 1)
	3 Vibration	Range of frequency : 10 ~ 500Hz Sweeping time : 15 min. Acceleration : 100m/s ² Sweeping direction : Each X,Y,Z axis Test time : 6 hr. (2hr./direction)	Test Method 403 Reference 1 Condition code B	5	(0 : 1)
	4 Shock	Maximum acceleration : 5000m/s ² Pulse width : 1.0msec. Direction : Each X,Y,Z axis Test time : 3 times/direction	Test Method 404 Condition code B	5	(0 : 1)
	5 Solderability	Solder temp. : 245±5 = Immersion time : 5±0.5sec. Test time : 1 time Solder Alloy : Sn-Ag-Cu Each terminal should be immersed in solder within 1~1.5mm from the body.	Test Method 303	5	(0 : 1)
	6 Resistance to Soldering Heat	Solder temp. : 260±5 = Immersion time : 10±1sec. Test time : 1 time Each terminal should be immersed in solder within 1~1.5mm from the body.	Test Method 302 Condition code A	5	(0 : 1)
Environment Tests	1 High Temperature Storage	Storage temp. : 125±5 = Test duration : 1000hr.	Test Method 201	5	(0 : 1)
	2 Low Temperature Storage	Storage temp. : -40±5 = Test duration : 1000hr.	Test Method 202	5	(0 : 1)
	3 Temperature Humidity Storage	Storage temp. : 85±2 = Relative humidity : 85±5% Test duration : 1000hr.	Test Method 103 Test code C	5	(0 : 1)
	4 Unsaturated Pressurized Vapor	Test temp. : 120: 2 = Test humidity : 85±5% Test duration : 96hr.	Test Method 103 Test code E	5	(0 : 1)
	5 Temperature Cycle	Test temp. : ┌ Low temp. -40: 5 = ├ High temp. 125: 5 = └ RT 5 ~ 35 = Dwell time : High ~ RT ~ Low ~ RT 1hr. 0.5hr. 1hr. 0.5hr. Number of cycles : 100 cycles	Test Method 105	5	(0 : 1)
	6 Thermal Shock	Test temp. : ┌ High temp. 100 ⁺⁰ = └ Low temp. 0 ⁻⁰ = Used liquid : Water with ice and boiling water Dipping time : 5 min. par each temp. Transfer time : 10 sec. Number of cycles : 10 cycles	Test Method 307 method >□ Condition code A	5	(0 : 1)

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Reliability Test Items

Test categories	Test items	Test methods and conditions	Reference norms EIAJ ED-4701 (Aug.-2001 edition)	Number of sample	Acceptance number
Endurance Tests	1 High temperature Reverse Bias (for Collector-Emitter)	Test temp. : Ta = 125: 5 = (Tj > 150 =) Bias Voltage : VC = 0.8×VCES Bias Method : Applied DC voltage to C-E VGE = 0V Test duration : 1000hr.	Test Method 101	5	(0 : 1)
	2 High temperature Bias (for gate)	Test temp. : Ta = 125: 5 = (Tj > 150 =) Bias Voltage : VC = VGE = +20V or -20V Bias Method : Applied DC voltage to G-E VCE = 0V Test duration : 1000hr.	Test Method 101	5	(0 : 1)
	3 Temperature Humidity Bias	Test temp. : 85: 2 °C Relative humidity : 85: 5% Bias Voltage : VC = 0.8×VCES Bias Method : Applied DC voltage to C-E VGE = 0V Test duration : 1000hr.	Test Method 102 Condition code C	5	(0 : 1)
	4 Intermitted Operating Life (Power cycle) (for IGBT)	ON time : 2 sec. OFF time : 18 sec. Test temp. : Δ Tj=100±5 deg Tj > 150 = , Ta=25±5 = Number of cycles : 15000 cycles	Test Method 106	5	(0 : 1)

Failure Criteria

Item	Characteristic	Symbol	Failure criteria		Unit	Note
			Lower limit	Upper limit		
Electrical characteristic	Leakage current	ICES	-	USL×2	mA	
		±IGES	-	USL×2	μA	
	Gate threshold voltage	VGE(th)	LSL×0.8	USL×1.2	mA	
	Saturation voltage	VCE(sat)	-	USL×1.2	V	
	Forward voltage	VF	-	USL×1.2	V	
	Thermal resistance	IGBT	Δ VGE or Δ VCE	-	USL×1.2	mV
FWD		Δ VF	-	USL×1.2	mV	
	Isolation voltage	Viso	Broken insulation		-	
Visual inspection	Visual inspection ┌ Peeling └ Plating and the others	-	The visual sample		-	

LSL : Lower specified limit.

USL : Upper specified limit.

Note : Each parameter measurement read-outs shall be made after stabilizing the components at room ambient for 2 hours minimum, 24 hours maximum after removal from the tests. And in case of the wetting tests, for example, moisture resistance tests, each component shall be made wipe or dry completely before the measurement.

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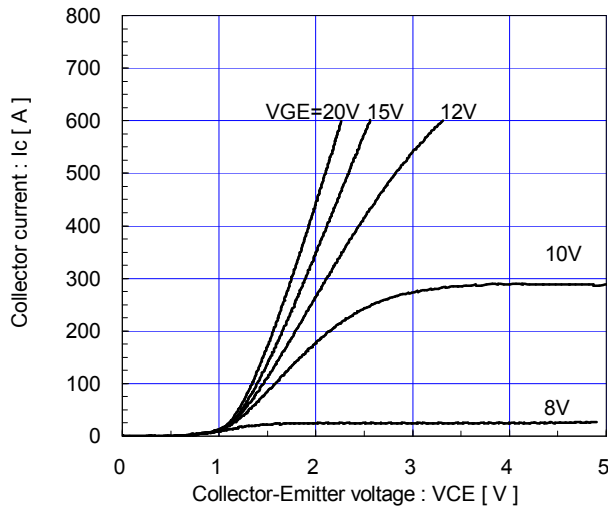
Reliability Test Results

Test categories	Test items	Reference norms EIAJ ED-4701 (Aug.-2001 edition)	Number of test sample	Number of failure sample
Mechanical Tests	1 Terminal Strength (Pull test)	Test Method 401 Method>□	5	0
	2 Mounting Strength	Test Method 402 method>□	5	0
	3 Vibration	Test Method 403 Condition code B	5	0
	4 Shock	Test Method 404 Condition code B	5	0
	5 Solderability	Test Method 303	5	0
	6 Resistance to Soldering Heat	Test Method 302 Condition code A	5	0
Environment Tests	1 High Temperature Storage	Test Method 201	5	0
	2 Low Temperature Storage	Test Method 202	5	0
	3 Temperature Humidity Storage	Test Method 103 Test code C	5	0
	4 Unsaturated Pressurized Vapor	Test Method 103 Test code E	5	0
	5 Temperature Cycle	Test Method 105	5	0
	6 Thermal Shock	Test Method 307 method >□ Condition code A	5	0
Endurance Tests	1 High temperature Reverse Bias	Test Method 101	5	0
	2 High temperature Bias (for gate)	Test Method 101	5	0
	3 Temperature Humidity Bias	Test Method 102 Condition code C	5	0
	4 Intermitted Operating Life (Power cycling) (for IGBT)	Test Method 106	5	0

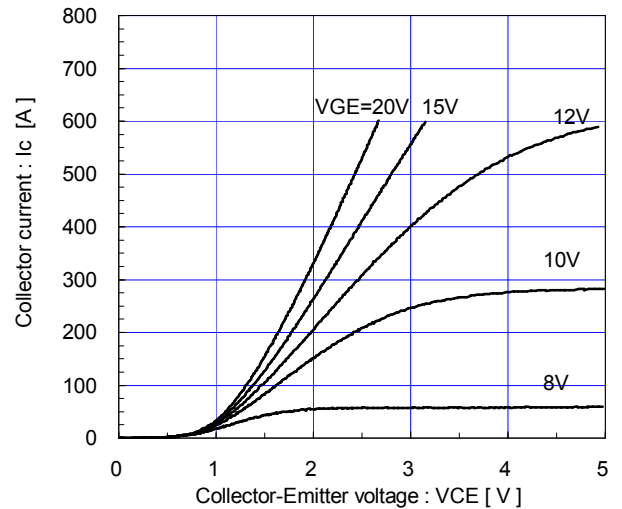
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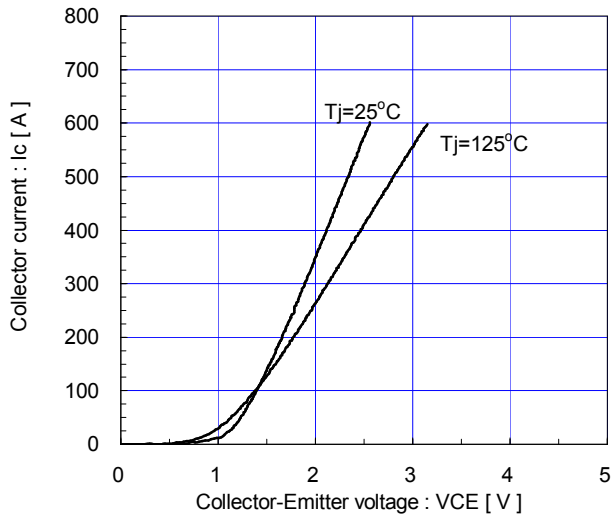
Collector current vs. Collector-Emittor voltage (typ.)
Tj=25°C / chip



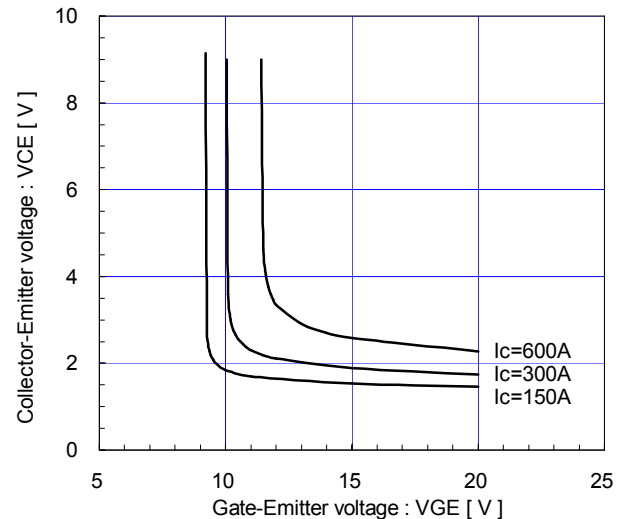
Collector current vs. Collector-Emittor voltage (typ.)
Tj=125°C / chip



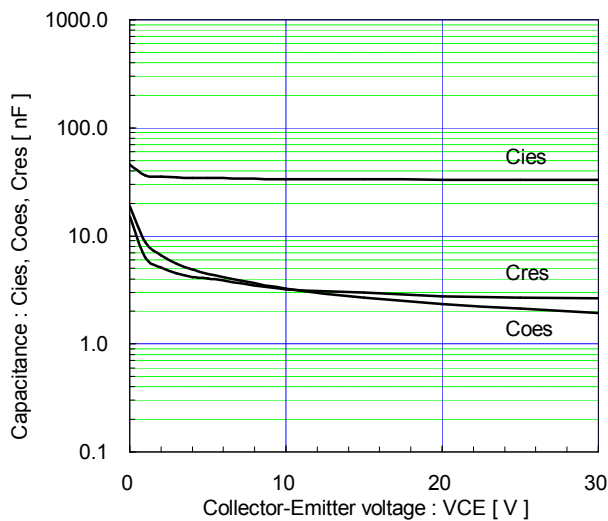
Collector current vs. Collector-Emittor voltage (typ.)
VGE=15V / chip



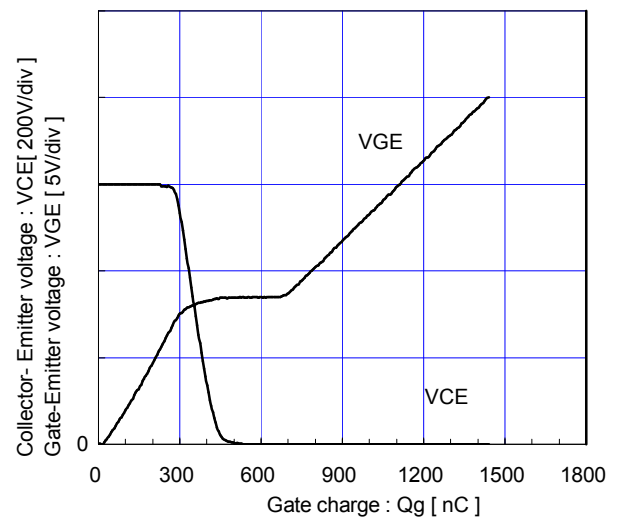
Collector-Emittor voltage vs. Gate-Emittor voltage (typ.)
Tj=25°C / chip



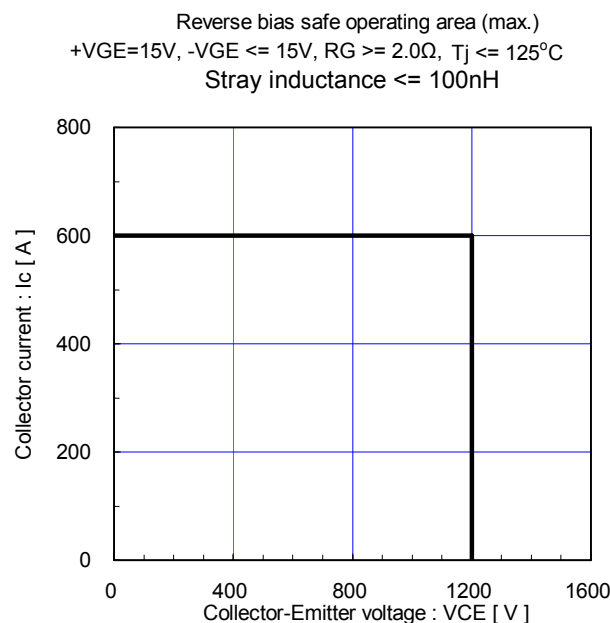
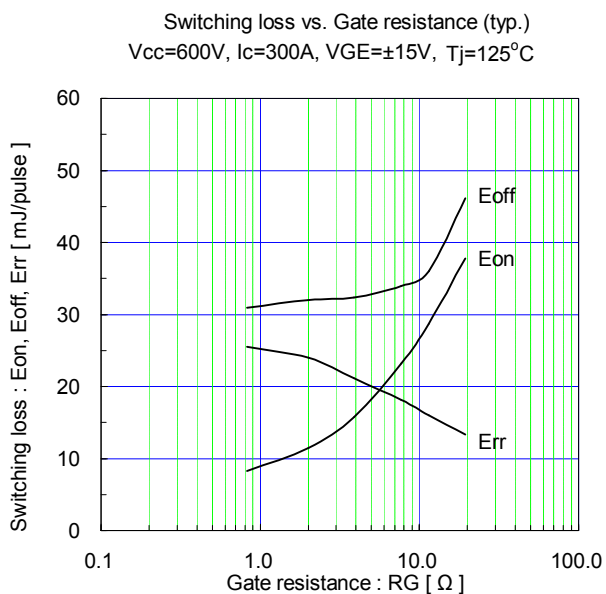
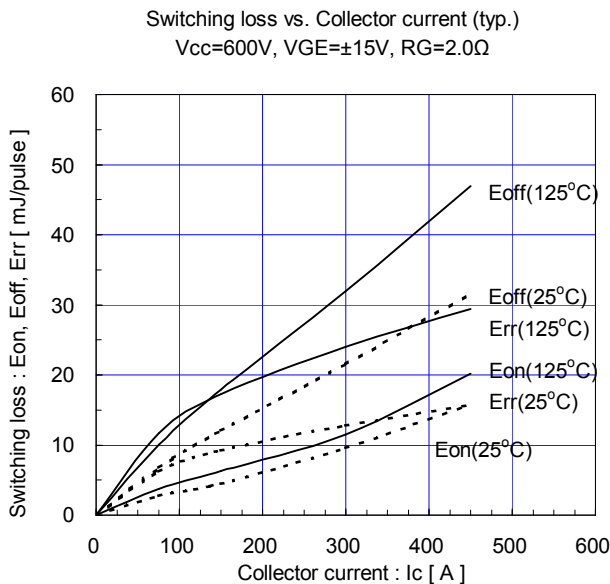
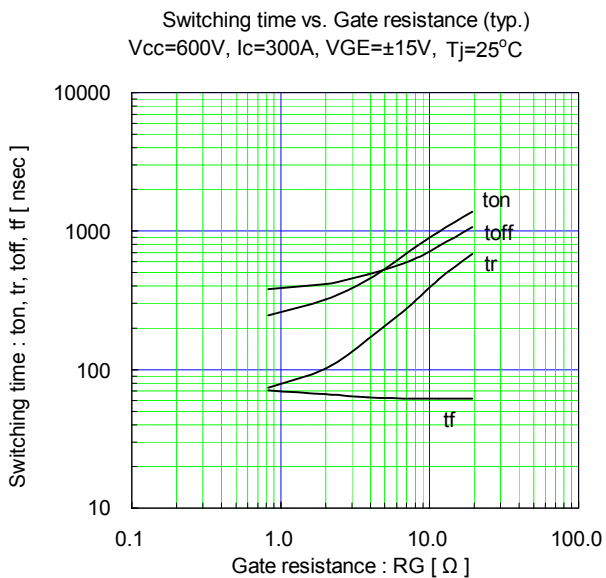
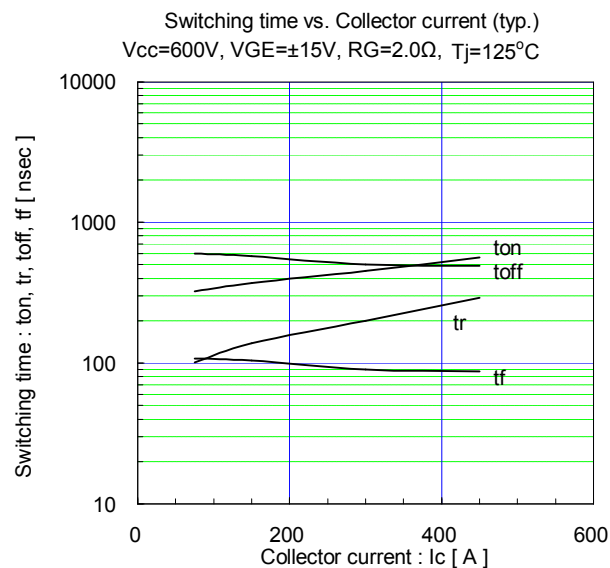
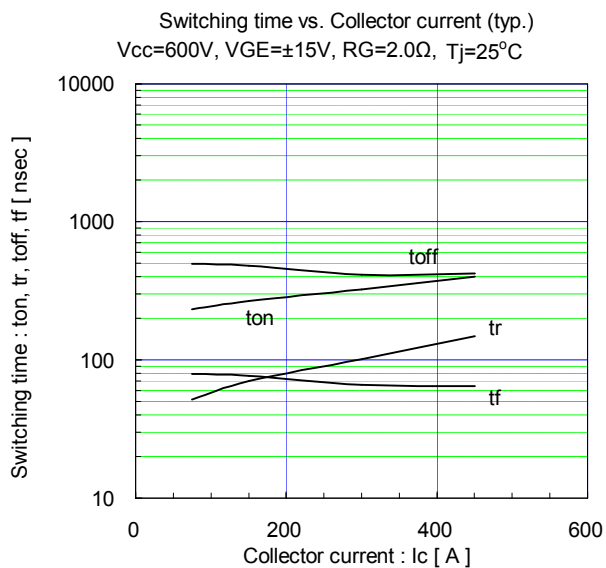
Capacitance vs. Collector-Emittor voltage (typ.)
VGE=0V, f=1MHz, Tj=25°C



Dynamic Gate charge (typ.)
Vcc=600V, Ic=300A, Tj=25°C

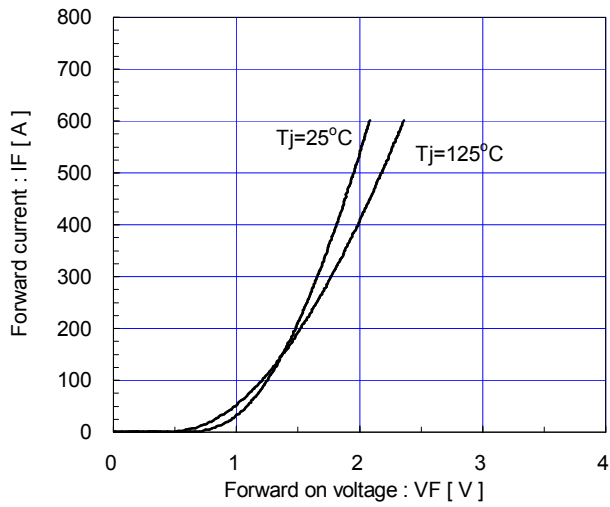


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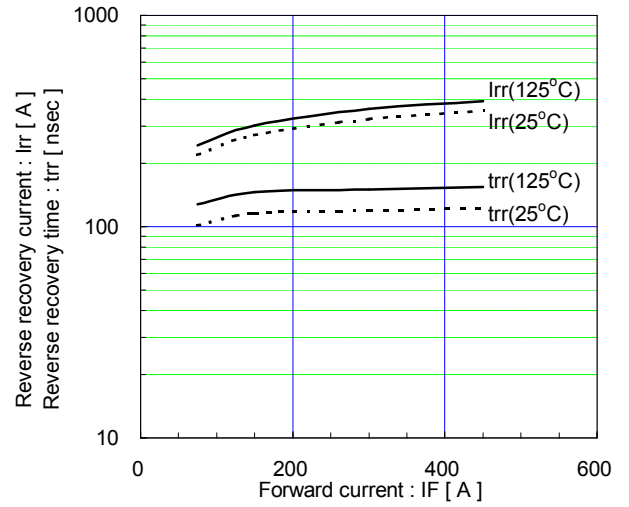


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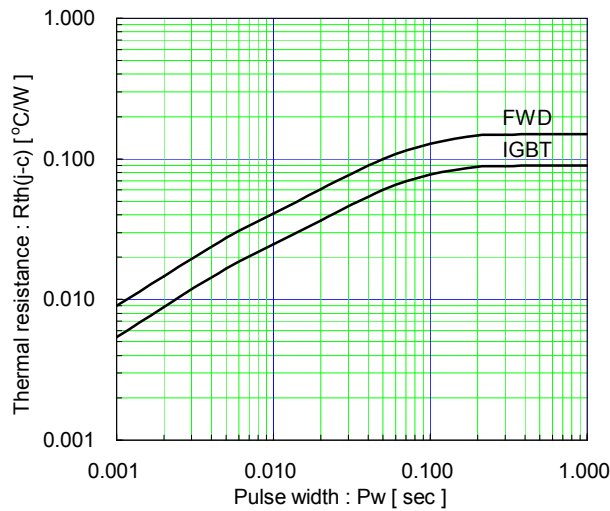
Forward current vs. Forward on voltage (typ.)
chip



Reverse recovery characteristics (typ.)
Vcc=600V, VGE=±15V, RG=2.0Ω

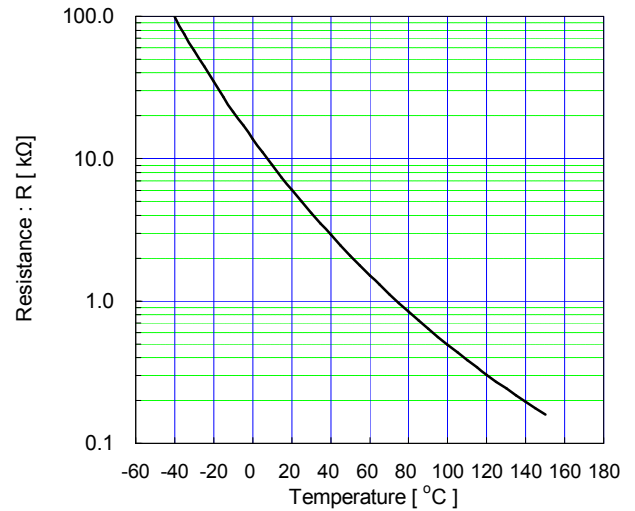


Transient thermal resistance (max.)



[Thermistor]

Temperature characteristic (typ.)



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Warnings

- Never add the excessive mechanical stress to the main or control terminals when the product is applied to equipments. The module structure may be broken.
 6 BU ' B.s -] B BM B.A% %5 BG □ □ %5 B.1 □ B-□ □ BU B-B-B* □ BB A%5 t 1g □ □ B.BN (H B B B B A
- In case of insufficient -VGE, erroneous turn-on of IGBT may occur. -VGE shall be set enough value to prevent this malfunction. (Recommended value : -VGE = -15V)
 1FB B B] B t B B B5 □ □ :-: : B □ / B B B B+ P □ □ BU BB □ (H B B B B A. P □ □ BU B B B B Q B. :-: : B2 □ □ B-□ B* .3 □ □ B B) □ B B A A B □ Q □ : w w :-: : w w :-: :
- In case of higher turn-on dv/dt of IGBT, erroneous turn-on of opposite arm IGBT may occur. Use this product in the most suitable drive conditions, such as +VGE, -VGE, RG to prevent the malfunction.
 B B B B B : w :-: : 7 B B B □ B] B B B C C C B □ P □ □ BU BB □ (H B B B B A. P □ □ BU B B B B Q B 1 B B B B B □ □ B :-: : w :-: : w : % B B B □ □ B B A
- This product may be broken by avalanche in case of VCE beyond maximum rating VCES is applied between C-E terminals. Use this product within its absolute maximum voltage.
 :-: : B V B B 5 □ □ B □ □ B B C □ □ A B] B B B B B V B B B) 6 □ □ B B □ □ B B B B A :-: : B2 □ □ B □ □ □ p B 1% □ □] B * B □ □ B B A

: : : : :

- Fuji Electric Device Technology is constantly making every endeavor to improve the product quality and reliability. However, semiconductor products may rarely happen to fail or malfunction. To prevent accidents causing injury or death, damage to property like by fire, and other social damage resulted from a failure or malfunction of the Fuji Electric Device Technology semiconductor products, take some measures to keep safety such as redundant design, spread-fire-preventive design, and malfunction-protective design.
 □ □ 5 □ □ B B B B t B B] B B B B B B2 B B ~ □ B 1 / B 5 H B 10 □ B. □ □ B B B B A A B B B A □ □ □ ~ □ B2 □ 4 B □ B B B W . G □ □ B B N □ □ B B B B A A □ □ 5 □ □ B B B t B B] B B B B ~ □ □ □ ~ □ B 1 □ 4 B B B2 G □ □ B A □ □ B B B) □ 0 P □ □ B B 4 % B B B W B. □ □ B B N □ □ B G □ □ B-□ □ BU B B B B B B B. □ 43.3 . □ □ 4 □ .3 . □ □ B. G □ □ 4 □ .3 . □ □ B-□ □ K □ □ B 1 B B B 1 □ □ BU B B) □ B B A
- The application examples described in this specification only explain typical ones that used the Fuji Electric Device Technology products. This specification never ensure to enforce the industrial property and other rights, nor license the enforcement rights.
 □ □ □ □ B. % 0 B B) B B N □ □ B2 A □ □ 5 □ □ B B B t B B] B B B B ~ □ □ BU B B □ + B-□ □ BU □ □ B B B B B * B B W □ □ □ □ B. B B B) □ □ □ □ A B B 1 □ □ □ B 1 □ □ B. □ □ B B N 4 B B B 2 □ □ B 1.5. BU B B B B * B 2 B B B B V A
- The product described in this specification is not designed nor made for being applied to the equipment or systems used under life-threatening situations. When you consider applying the product of this specification to particular used, such as vehicle-mounted units, shipboard equipment, aerospace equipment, medical devices, atomic control systems and submarine relaying equipment or systems, please apply after confirmation of this product to be satisfied about system construction and required reliability.
 □ □ □ □ B. % 0 B B C ~ □ B2 A □ □ B. B B B B B B B B A □ □ B * □ □ B B C N z B B B B 2 B t B B B. B B B C B B B U B B B) .3 . □ □ B ~ 1 g B B C B B 1 * B 2 B B B B V A □ □ □ □ B 1 ~ □ B 0 □ □ z A)) ~ A) v] □ □ A □ □ n □ □ z A □ 6 □ □ □ A □) □ □ z B B B B 2 B t B B B B A □ 1 B B 1 B □ B B □ . □ B 14 B 2 A B t B t B □ □ □ B 6 □ □ / B.) / B B B B B □ . z B 1 □ A B □ □ B B A

If there is any unclear matter in this specification, please contact Fuji Electric Device Technology Co., Ltd.