

# 2SK3932-01MR



## N-CHANNEL SILICON POWER MOSFET

Outline Drawings (mm) 200406

### FUJI POWER MOSFET Super FAP-G Series

#### Features

- High speed switching
- Low on-resistance
- No secondary breakdown
- Low driving power
- Avalanche-proof

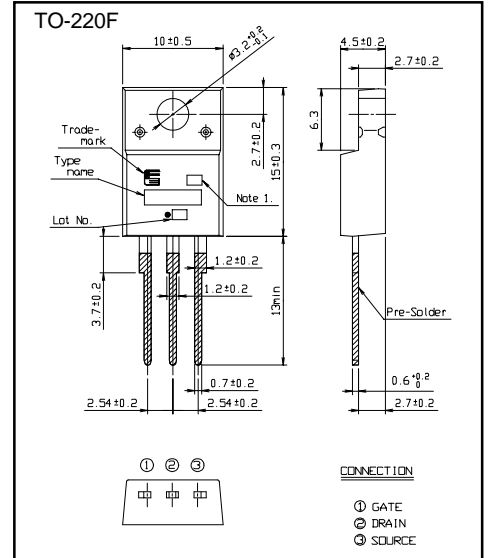
#### Applications

- Switching regulators
- DC-DC converters
- UPS (Uninterruptible Power Supply)

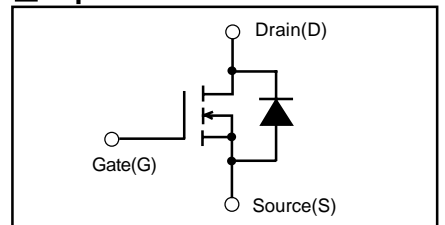
#### Maximum ratings and characteristic

Absolute maximum ratings  
(T<sub>c</sub>=25°C unless otherwise specified)

Item	Symbol	Ratings	Unit	Remarks
Drain-source voltage	V <sub>DS</sub>	500	V	
	V <sub>DSX</sub>	500	V	V <sub>GS</sub> =-30V
Continuous Drain Current	I <sub>D</sub>	11	A	
Pulsed Drain Current	I <sub>D(puls)</sub>	±44	A	
Gate-Source Voltage	V <sub>GS</sub>	±30	V	
Maximum Avalanche current	I <sub>AR</sub>	11	A	Note *1
Non-Repetitive	E <sub>AS</sub>	453.9	mJ	Note *2
Maximum Avalanche Energy	E <sub>AR</sub>	6.0	mJ	Note *3
Maximum Drain-Source dV/dt	dV <sub>DS</sub> /dt	20	kV/μs	V <sub>DS</sub> ≤500V
Peak Diode Recovery dV/dt	dV/dt	5	kV/μs	Note *4
Max. Power Dissipation	P <sub>D</sub>	60	W	T <sub>c</sub> =25°C
		2.16		T <sub>a</sub> =25°C
Operating and Storage Temperature range	T <sub>ch</sub>	+150	°C	
	T <sub>stg</sub>	-55 to +150	°C	
Isolation Voltage	V <sub>ISO</sub>	2	kVrms	t=60sec, f=60Hz



#### Equivalent circuit schematic



Note \*1: T<sub>ch</sub> ≤ 150°C, Repetitive and Non-repetitive

Note \*2: Starting T<sub>ch</sub>=25°C, I<sub>AS</sub>=4.4A, L=43mH,

V<sub>CC</sub>=50V, R<sub>G</sub>=50Ω

E<sub>AS</sub> limited by maximum channel temperature and Avalanche current.

See to the 'Avalanche Energy' graph

Note \*3: Repetitive rating: Pulse width limited by maximum channel temperature.

See to the 'Transient Thermal impedance' graph.

Note \*4: I<sub>F</sub> ≤ -I<sub>D</sub>, -di/dt=50A/μs, V<sub>CC</sub> ≤ BV<sub>DSS</sub>, T<sub>ch</sub> ≤ 150°C

#### Electrical characteristics (T<sub>c</sub>=25°C unless otherwise specified)

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA V <sub>GS</sub> =0V	500			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	I <sub>D</sub> =250μA V <sub>DS</sub> =V <sub>GS</sub>	3.0		5.0	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =500V V <sub>GS</sub> =0V			25	μA
		V <sub>DS</sub> =400V V <sub>GS</sub> =0V			250	μA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V V <sub>DS</sub> =0V		10	100	nA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =5.5A V <sub>GS</sub> =10V		0.57	0.70	Ω
Forward Transconductance	g <sub>fs</sub>	I <sub>D</sub> =5.5A V <sub>DS</sub> =25V	4.5	9.0		S
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V		950	1425	pF
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> =0V		130	195	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f=1MHz		6.0	9.0	pF
Turn-On Time t <sub>on</sub>	td(on)	V <sub>CC</sub> =300V I <sub>D</sub> =5.5A		16	24	ns
	t <sub>r</sub>	V <sub>GS</sub> =10V		6.0	9.0	ns
Turn-Off Time t <sub>off</sub>	td(off)	R <sub>GS</sub> =10Ω		33	50	ns
	t <sub>f</sub>			5.5	8.3	ns
Total Gate Charge	Q <sub>G</sub>	V <sub>CC</sub> =250V		25	38	nC
Gate-Source Charge	Q <sub>GS</sub>	I <sub>D</sub> =11A		10	15	nC
Gate-Drain Charge	Q <sub>GD</sub>	V <sub>GS</sub> =10V		8.0	12	nC
Diode forward on-voltage	V <sub>SD</sub>	I <sub>F</sub> =11A V <sub>GS</sub> =0V T <sub>ch</sub> =25°C		1.10	1.50	V
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> =11A V <sub>GS</sub> =0V		650		ns
Reverse recovery charge	Q <sub>rr</sub>	-di/dt=100A/μs T <sub>ch</sub> =25°C		5.5		μC

#### Thermal characteristics

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal resistance	R <sub>th(ch-c)</sub>	channel to case			2.083	°C/W
	R <sub>th(ch-a)</sub>	channel to ambient			58	°C/W

## Characteristics

