

F5041

FUJI Intelligent Power MOSFET

Intelligent Power MOSFET

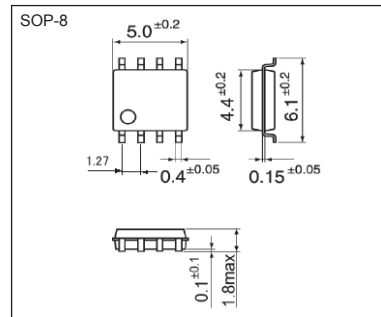
■ Features

- Two N-ch power MOSFET circuits
- Over temperature protection
- Short circuit protection
- Low on-resistance
- High speed switching

■ Applications

- Solenoid driver
- Lamp driver
- Replacements for fuse and relay

■ Outline drawings [mm]



■ Connection

- ① SOURCE 1
- ② GATE 1
- ③ SOURCE 2
- ④ GATE 2
- ⑤ ⑥ DRAIN 2
- ⑦ ⑧ DRAIN 1

■ Maximum ratings and characteristics

● Absolute maximum ratings (at Tc=25°C, unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain-source voltage	V _{DS}	40	V	DC
Gate-source voltage	V _{GS}	7	V	DC
Gate-source minus voltage	-V _{GSS}	1.5	V	R _G =100Ω
Continuous drain current	I _D	1	A	-
Maximum power dissipation	P _D	1.5	W	*
Operating junction temperature	T _J	150	°C	-
Storage temperature range	T _{stg}	-55 ~ 150	°C	-
Single pulse inductive load switch-off energy dissipation	E _{clt}	25	mJ	T _J =150°C, I _D =0.5A Single pulse, dv/dt≤10V/μs

Note * : Surface mounted on 1000mm²PCB (FR-4), 2ch on simultaneously

● Electrical characteristics (at Tc=25°C unless otherwise specified)

Description	Symbol	Conditions	min.	typ.	max.	Unit
Drain-source clamp voltage	V _{DSS}	I _D =1mA, V _{GS} =0V	40	-	60	V
Gate threshold voltage	V _{GS(th)}	I _D =10mA, V _{DS} =13V	1.53	-	2.8	V
Operation gate voltage (protection circuit operates)	V _{GS(op)}	-	2.8	-	7.0	V
Zero gate voltage drain current	I _{DSS(-VGS)}	V _{DS} =16V, V _{GS} =0~1.5V V _{DS} =30V, V _{GS} =0~1.5V	-	-	15 35	μA
Zero gate minus voltage drain current	I _{DSS}	V _{DS} =16V, V _{GS} =-1.5V, R _G =100Ω V _{DS} =30V, V _{GS} =-1.5V, R _G =100Ω	-	-	12 30	μA
Gate-source leakage current	I _{GS(m)} I _{GS(um)}	V _{GS} =5V** V _{GS} =5V, T _J >150°C***	-	-	250 300	μA
Drain-source on-state resistance	R _{DS(on)}	I _D =0.5A, V _{GS} =5V	-	-	600	mΩ
Turn-on time	t _{on}	V _{DS} =13V, I _D =0.5A, V _{GS} =5V	-	-	50	μs
Turn-off time	t _{off}		-	-	50	μs
Over-temperature protection	T _{trip}	V _{GS} =5V	150	-	-	°C
Short circuit protection	I _{oc}	V _{GS} =5V	1.5	-	-	A

Note ** : Under normal operation

Note *** : Under self protection (Short circuit ~ Short circuit protection ~ Over-temperature protection)

● Electrical characteristics (at Tc=-40~105°C unless otherwise specified)

Description	Symbol	Conditions	min.	typ.	max.	Unit
Drain-source clamp voltage	V _{DSS}	I _D =1mA, V _{GS} =0V	38	-	62	V
Gate threshold voltage	V _{GS(th)}	I _D =10mA, V _{DS} =13V	1.5	-	3.0	V
Operation gate voltage (protection circuit operates)	V _{GS(op)}	-	3.0	-	6.8	V
Zero gate voltage drain current	I _{DSS(-VGS)}	V _{DS} =16V, V _{GS} =0~1.5V V _{DS} =30V, V _{GS} =0~1.5V	-	-	25 50	μA
Zero gate minus voltage drain current	I _{DSS}	V _{DS} =16V, V _{GS} =-1.5V, R _G =100Ω V _{DS} =30V, V _{GS} =-1.5V, R _G =100Ω	-	-	20 50	μA
Gate-source leakage current	I _{GS(m)} I _{GS(um)}	V _{GS} =5V** V _{GS} =5V, T _J >150°C***	-	-	300 350	μA
Drain-source on-state resistance	R _{DS(on)}	I _D =0.5A, V _{GS} =5V	-	-	920	mΩ
Turn-on time	t _{on}	V _{DS} =13V, I _D =0.5A, V _{GS} =5V	-	-	70	μs
Turn-off time	t _{off}		-	-	50	μs
Short circuit protection	I _{oc}	V _{GS} =5V	0.7	-	-	A

Note ** : Under normal operation

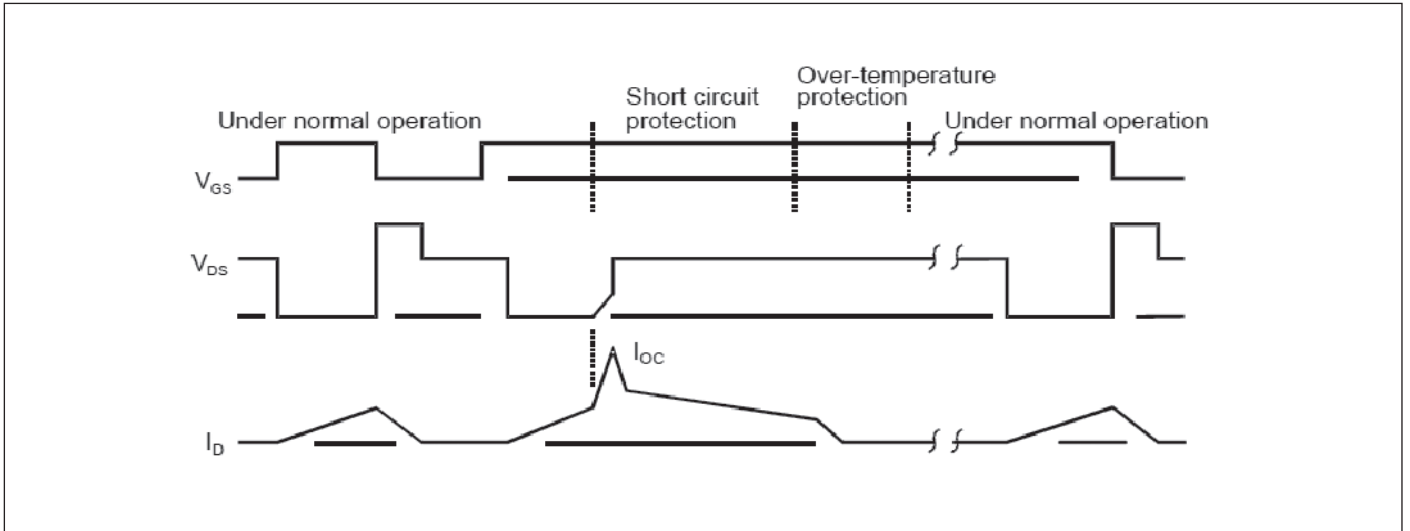
Note *** : Under self protection (Short circuit ~ Short circuit protection ~ Over-temperature protection)

● Thermal resistance

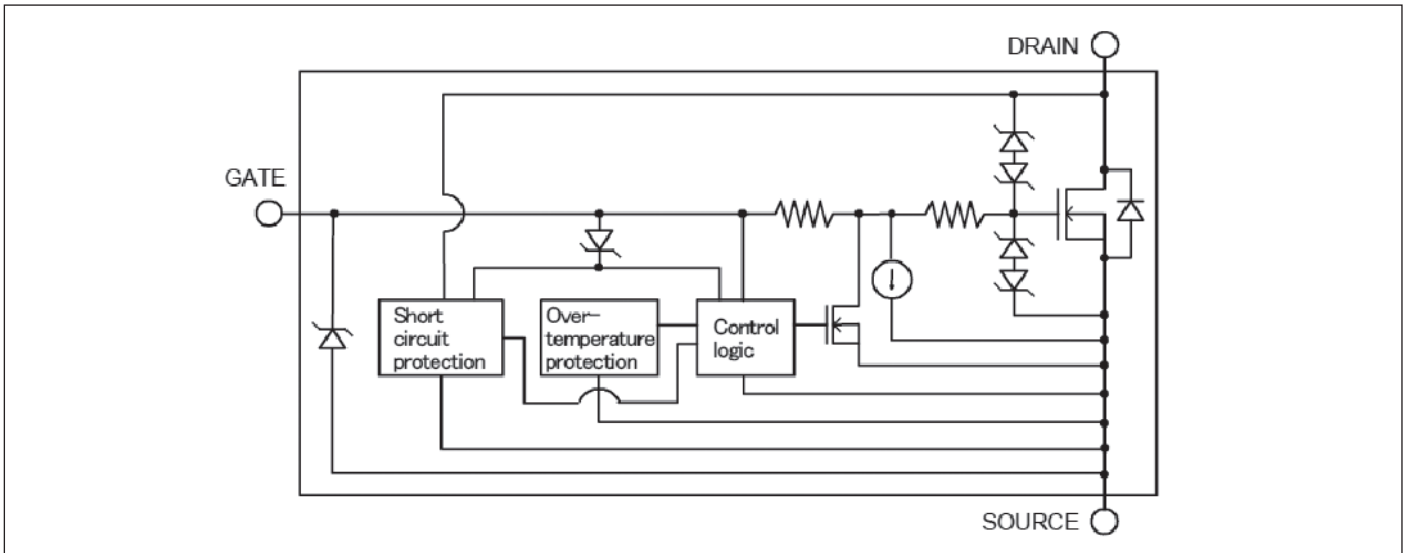
Description	Symbol	Test conditions	min.	typ.	max.	Unit
Thermal resistance	R th (j-a)	Junction-ambient*	-	-	83	°C/W

Note * : Surface mounted on 1000mm²PCB (FR-4), 2ch on simultaneously

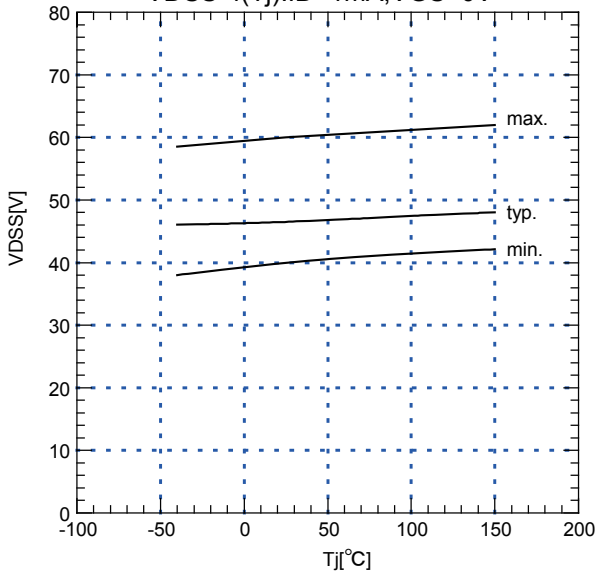
■ Timing chart



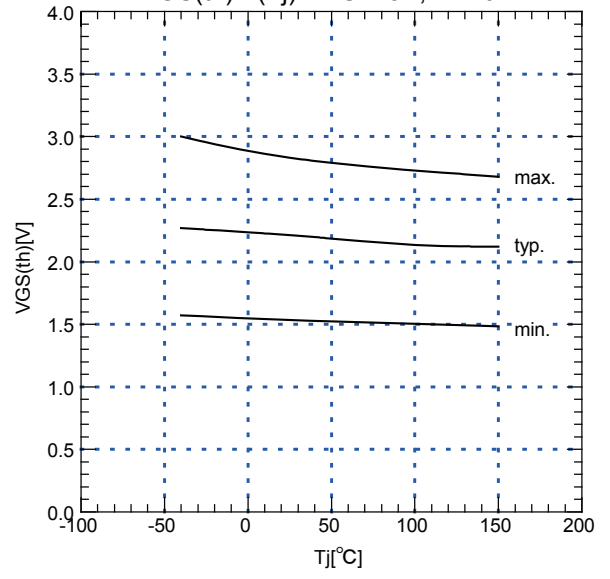
■ Circuit block diagram



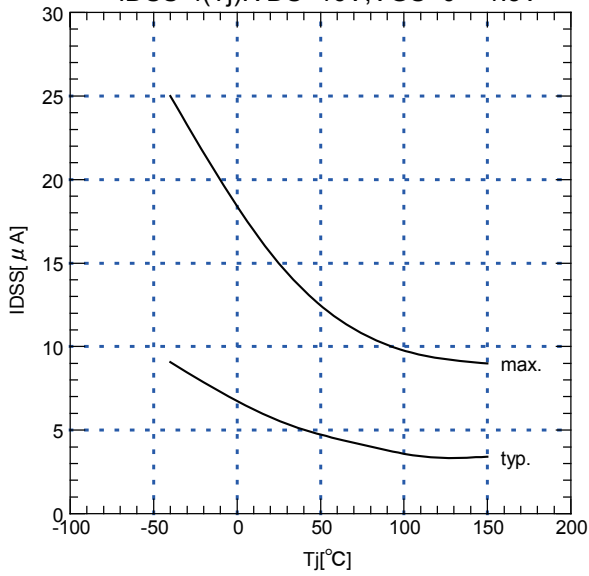
Drain-source breakdown voltage
 $V_{DSS}=f(T_j): I_D=1\text{mA}, V_{GS}=0\text{V}$



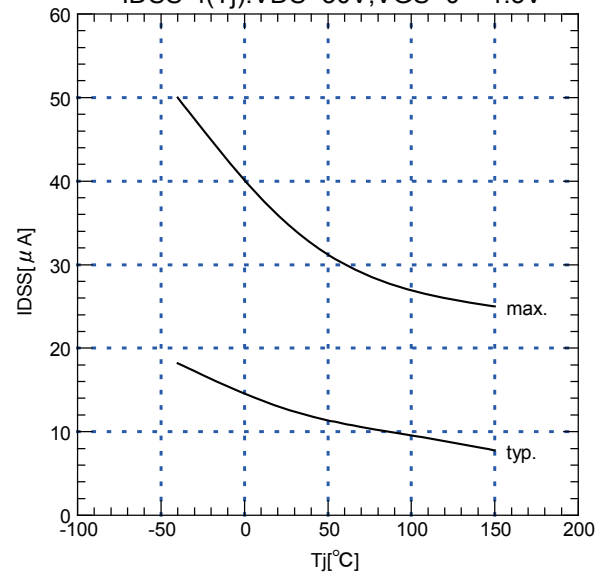
Gate threshold voltage
 $V_{GS(th)}=f(T_j): V_{DS}=13\text{V}, I_D=10\text{mA}$



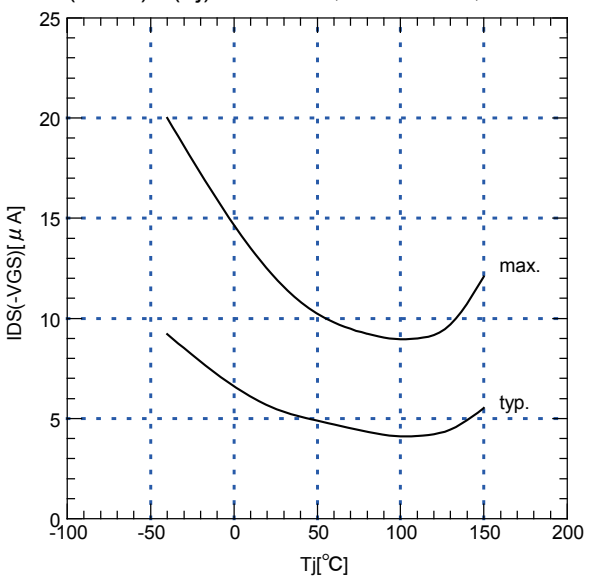
Zero gate voltage drain current
 $I_{DSS}=f(T_j): V_{DS}=16\text{V}, V_{GS}=0 \sim 1.5\text{V}$



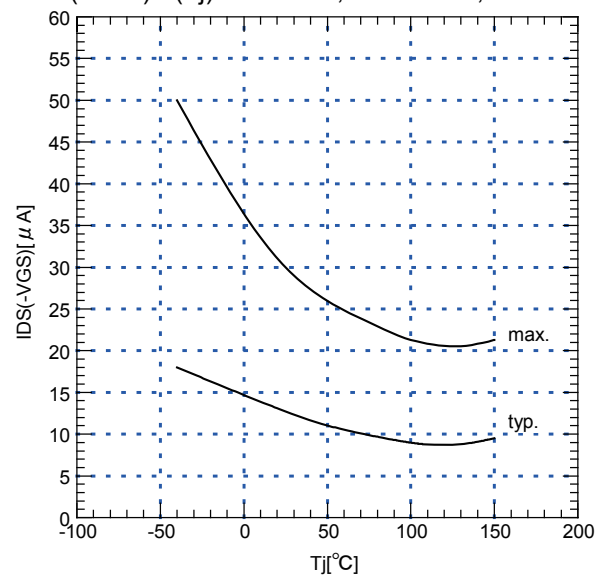
Zero gate voltage drain current
 $I_{DSS}=f(T_j): V_{DS}=30\text{V}, V_{GS}=0 \sim 1.5\text{V}$

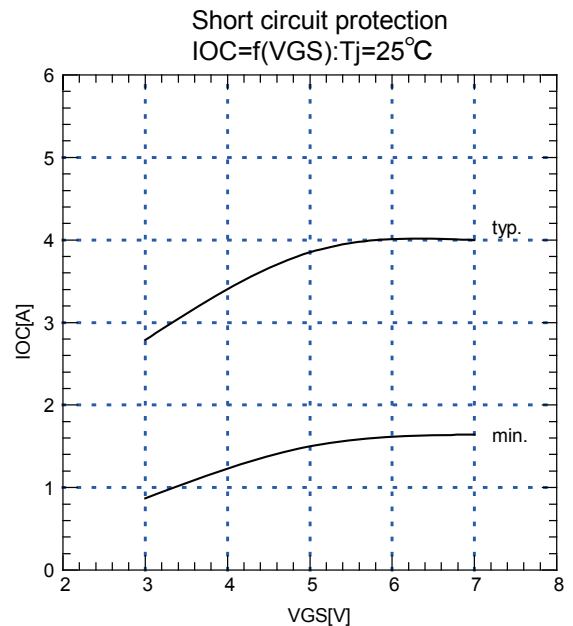
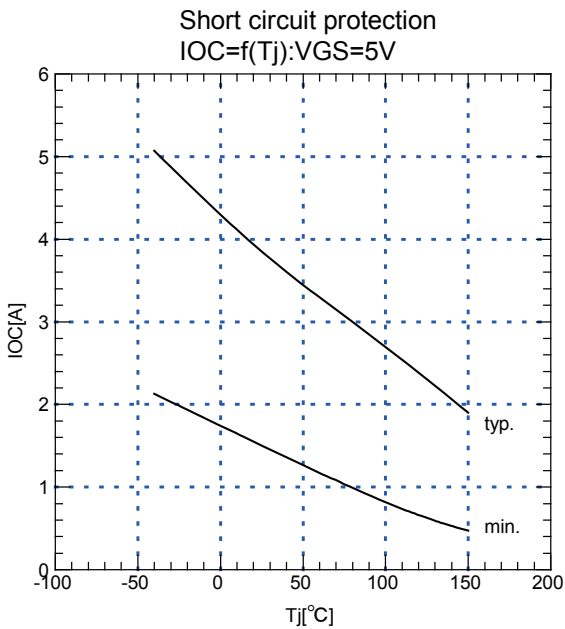
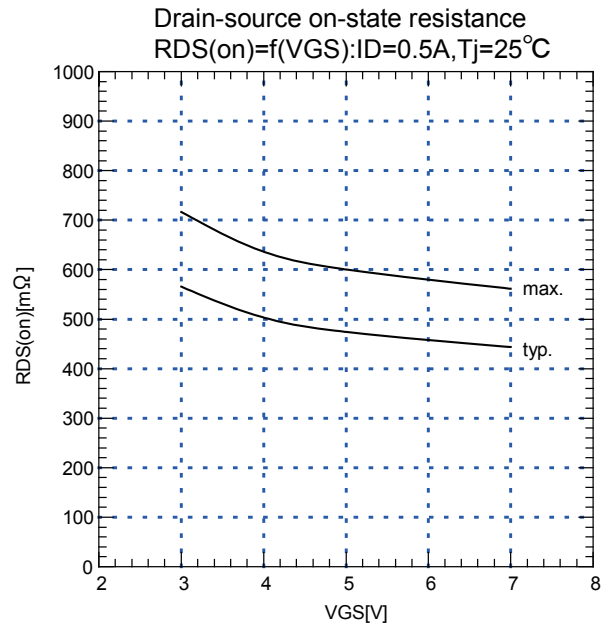
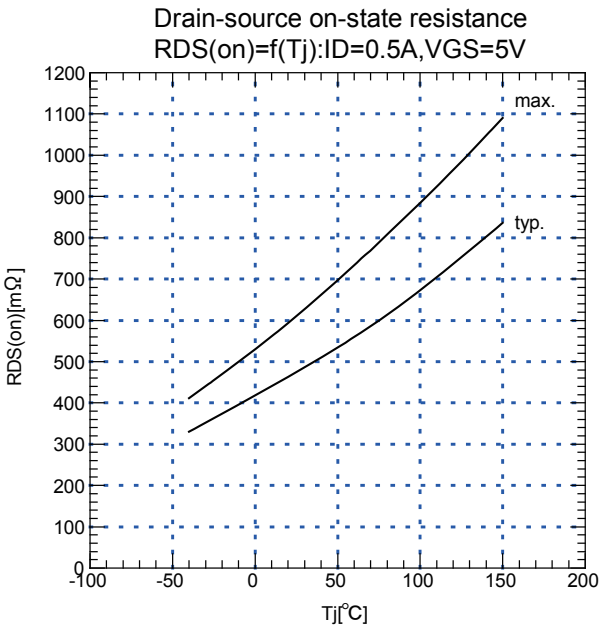
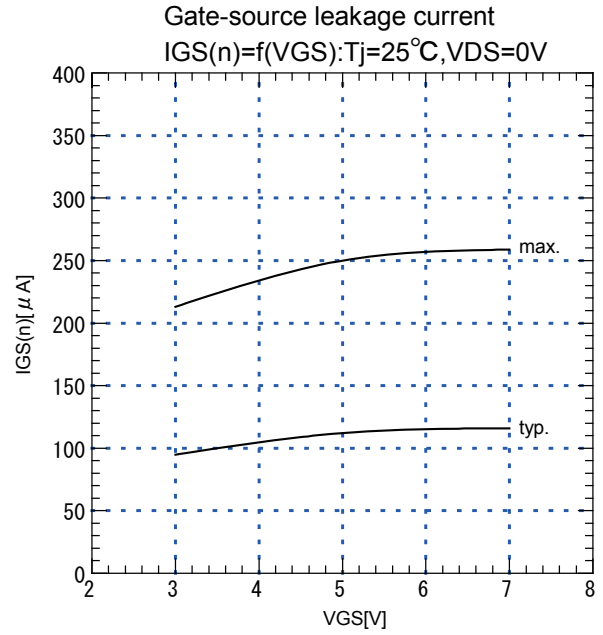
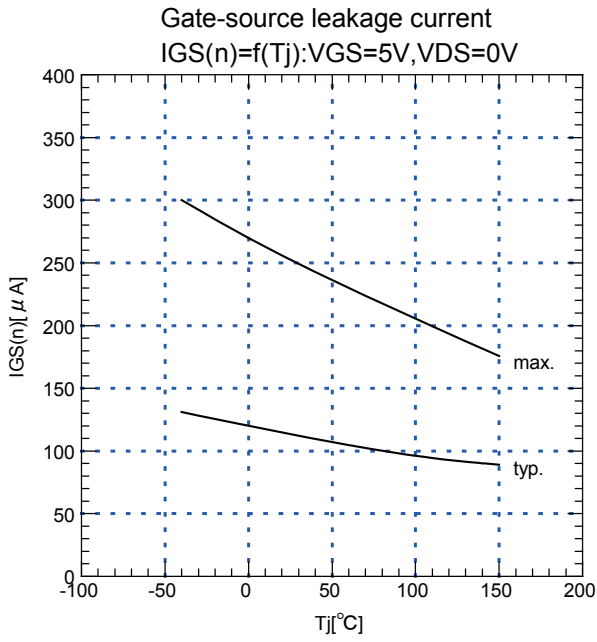


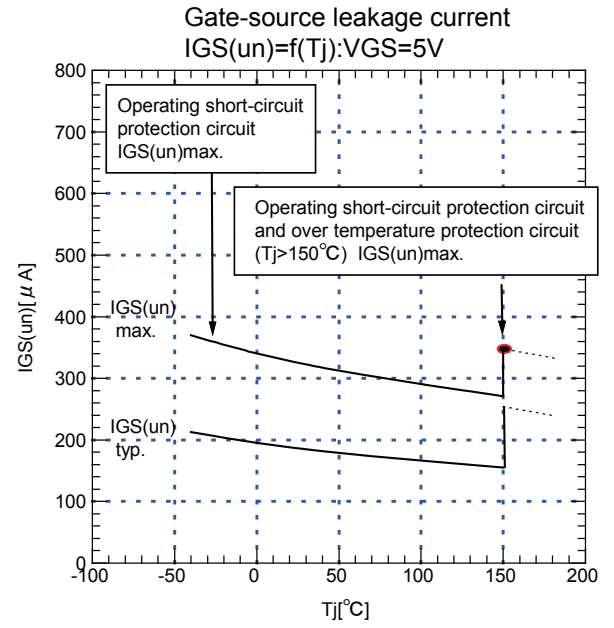
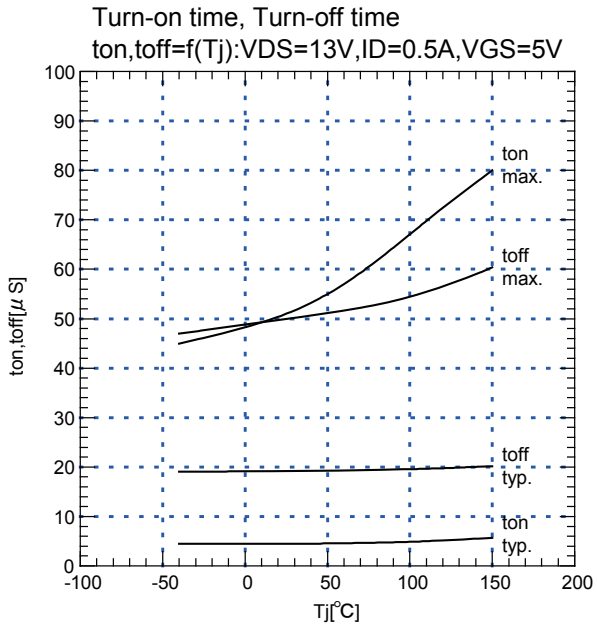
Zero gate minus voltage drain current
 $I_{DS(-VGS)}=f(T_j): V_{DS}=16\text{V}, V_{GS}=-1.5\text{V}, R_G=100\Omega$



Zero gate minus voltage drain current
 $I_{DS(-VGS)}=f(T_j): V_{DS}=30\text{V}, V_{GS}=-1.5\text{V}, R_G=100\Omega$







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