■ Description

FA3630V is a DC-DC converter controller. This IC can directly drive a Nch/Pch-MOSFET. This IC is suitable to reduce converter size because it has many functions in a small package TSSOP.

■ Features

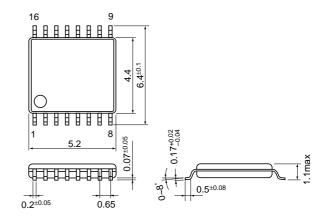
- Low input voltage: 2.5V to 6.5V
- 2-channels PWM control: Rail-to-rail output
 Output impedance of output stage = 3.5Ω (typ.)
 Nch-MOSFET driving (Ch1)
 Nch/Pch-MOSFET driving (Ch2)(selected by SEL pin)
- Low power consumption: 0.7mA (typ.)
- ON/OFFcontrol: Standby current= 0.1µA (typ.)
- ±2% high accuracy reference voltage
- Adjustable soft start time and maximum duty cycle
- Adjustable built-in timer latch for short circuit protection
- Wide range of operation frequency: 100kHz to 1MHz
- Package: TSSOP-16 (Thin and small)

■ Applications

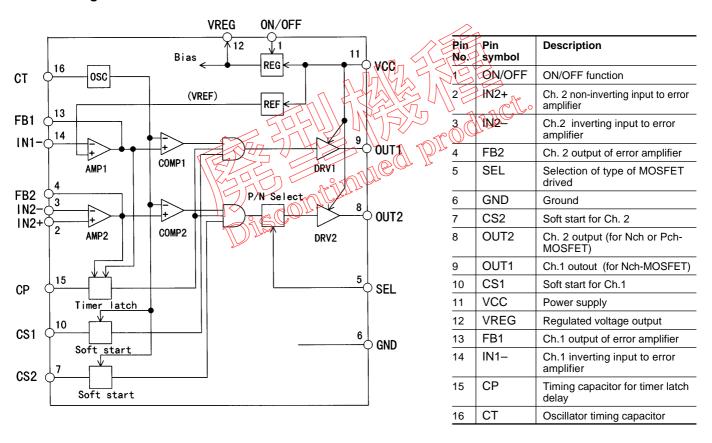
• Power supply for general equipment

■ Dimensions, mm

• TSSOP-16



■ Block diagram



■ Absolute maximum ratings

Item	Symbol	Rating	Unit
Power supply voltage	Vcc	6.5	V
Source peak current of OUT1/2	lout+	-500	mA
Sink peak current of OUT1/2	Іоит–	500	mA
Input voltage for logic input	VLOG	-0.3 to +6.5	V
Output current of VREG	IREGMAX	-10	mA
Total power dissipation*	Pd	300	mW
Junction temperature	TJ	125	°C
Ambient temperature	Тор	-20 to +85	°C
Storage temperature	Tstg	-40 to +125	°C

■ Recommended operating conditions

Item	Symbol	Min.	Max.	Unit
Power supply voltage	Vcc	2.5	5.5	V
Input voltage for logic input	VLOG	0	5.5	V
Oscillation frequency	fosc	100	1000	kHz

■ Electrical characteristics (Vcc=3.3V, Ta=25°C, CT=100pF)

Regulated voltage section for internal control blocks

Item	Symbol	Test condition	Min.	Тур.	Max.	Unit
Regulated voltage	VREG	IREG=no load	2.156	2.200	2.244	V
Variation with output current	VRGOUT	VREG=0 to 5mV			5	mV
Variation with supply voltage	VRGLIN	Vcc=2.5 to 5.5V			8	mV
Variation with temperature	VRGTa	Ta=-20 to +25°C		±0.5		%
		Ta=+25 to +85°C		±0.5		%

Reference voltage section

Item	Symbol	Test condition	Min.	Тур.	Max.	Unit
Reference voltage	VREF		0.98	1.00	1.02	V
Variation with supply voltage	VRGLIN	Vcc=2.5 to 5.5V		1	5	mV
Variation with temperature	VRGTa 🔨	Ta=20 to +25°C	2	±0.5		%
	17711	Ta=+25 to +85°C	1270	₹0.5		%

Oscillator section

Item	15/25	Symbol	Test condition	Min.	Тур.	Max.	Unit
Oscillation frequency	MARIE	fosc 1	© ⊤ ⊭ 100pF	437	485	533	kHz
High level voltage		VOSCH	Ст=100рF		1.39		V
Low level voltage	กร์เ	Vescl	Ст=100рF		0.77		V
Variation with supply voltage	191	fd∨	Vcc=2.5 to 5.5V		±1	±5	%
Variation with temperature		f _d т	Ta=-20 to +25°C		±5		%
			Ta=+25 to +85°C		±5		%

Error amplifier section

Item	Symbol	Test condition	Min.	Тур.	Max.	Unit
Input offset voltage	VIOF			3	10	mV
Common mode input voltage	Vісом		0.2		1.5	٧
DC open loop gain	Avol		70	75		dB
Unity gain band width	fτ			1.6		MHz
Output sink current	IFBL	VFB=0.5V	2.0	2.5	3.0	mA
Output source current	Ігвн	VFB=VREG-0.5V	-160	-125	-90	μΑ

^{*} Ta ≦ 25°C

ON/OFF logic input signal section

Item	Symbol	Test condition	Min.	Тур.	Max.	Unit
Input range for ON mode	Vonh	Vcc=2.5V to 5.5V	2.0			V
Input range for OFF mode	Vonl	Vcc=2.5V to 5.5V			0.5	V

SEL logic input signal section

Item	Symbol	Test condition	Min.	Тур.	Max.	Unit
Input range for driving Nch-MOSFET	VSELH		Vcc-0.5			V
Input range for driving Pch-MOSFET	VSELL				0.5	V

Timer latch protection section

Item	Symbol	Test condition	Min.	Тур.	Max.	Unit
Threshold voltage of CP	Vсртн		1.55	1.70	1.85	V
Charge current of CP	Іср	Vcp=Vreg-0.5V	-2.5	-2.1	-1.7	μА
Threshold voltage of FB1/FB2	VFBTL		1.56	1.73	1.90	V

Soft-start section

Item	Symbol	Test condition	Min.	Тур.	Max.	Unit
Threshold voltage of CS1/2	Vcs o	Duty cycle=0%	0.74	0.79	0.84	V
Threshold voltage of CS1/2	Vcs 100	Duty cycle=100%	1.29	1.34	1.39	V

Output section

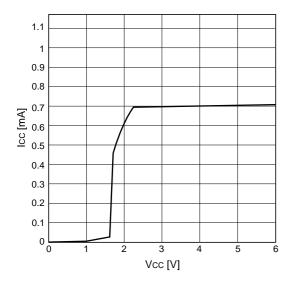
Item		Symbol	Test condition	Min.	Тур.	Max.	Unit
ON resistance	OUT1 (High / Low)	Ron	IouT=±100mA		3.5	7	Ω
	OUT2 (High / Low)		V¢¢=3.3∀		3.5	7	Ω
Rise time	OUT1	tr	CLOAD⇒1000pF		25	35	ns
	OUT2	Jall 55	Wec=3:3V	t.º	25	35	ns
Fall time	OUT1	() th	CtoA0=1000pF		25	35	ns
	OUT2		Vcc=3.3V 1€		25	35	ns

Overall device

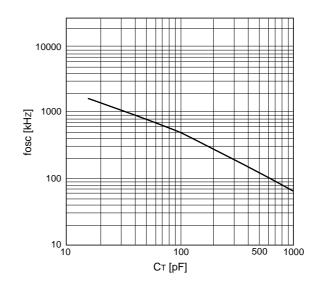
Item		Symbol	Test condition	Min.	Тур.	Max.	Unit
Standby current	1 Sign	Tcco	ON/OFF=GND		0.1	5	μΑ
Operating average current	20) No	Icc	OUT1, OUT2: Open		0.7	1	mA
			Output duty=100%				
		(Reference value)	OUT1, OUT2: Open		1.5		mA
			IN- and FB: Shorted				

■ Characteristic curves (Ta=25°C)

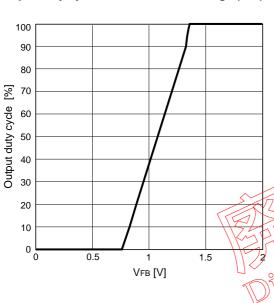
Supply current (Icc) vs. supply voltage (Vcc)



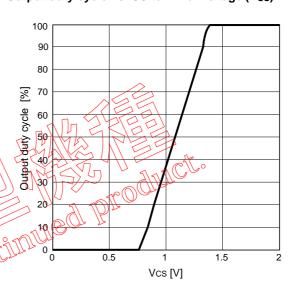
Oscillation frequency (fosc) vs. timing capactior(CT)



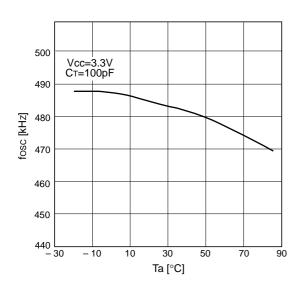
Output duty cycle vs. FB terminal voltage (VFB)



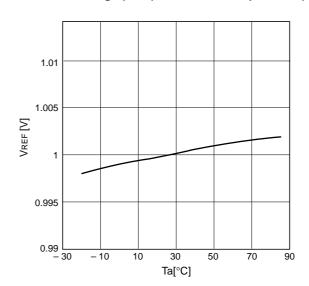
Output duty cycle vs. CS terminal voltage (Vcs)



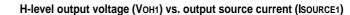
Oscillation frequency (fosc) vs. ambient temperature (Ta)

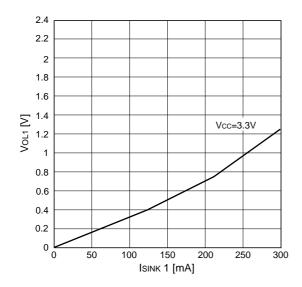


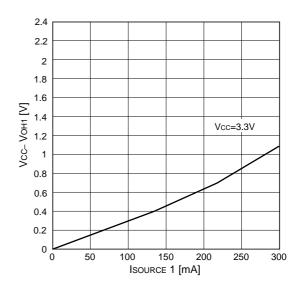
Reference voltage (VREF) vs. ambient temperature (Ta)



L-level output voltage (VoL1) vs. output sink current (Isink1)

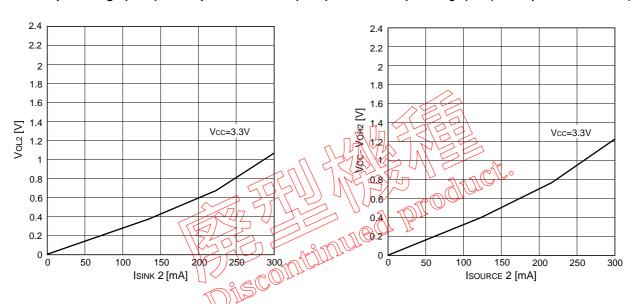






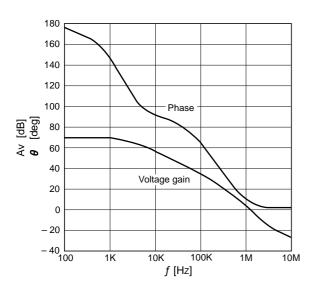
L-level output voltage (VoL2) vs. output sink current (Isink2)

H-level output voltage (VoH2) vs. output source current (ISOURCE2)

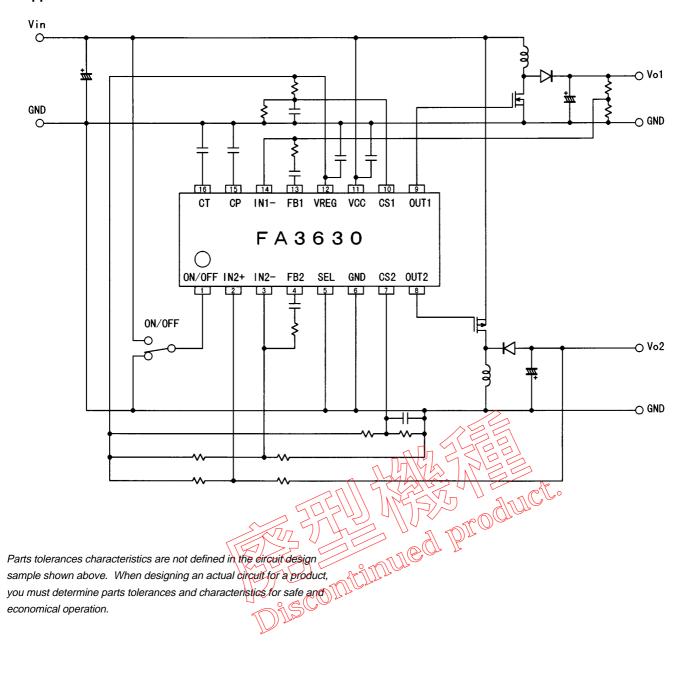


Error amplifier voltage gain (Av) / phase (θ) vs. frequency (f)

Condition: Open loop



■ Application circuit



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