

SPECIFICATION

Device Name : Power MOSFET

Type Name : FML19N50G

Spec. No. : **MS5F6439**

Date : *Oct.-25-2005*

This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

	DATE	NAME	APPROVED	Fuji Electric Device Technology Co.,Ltd.		
DRAWN	<i>Oct.-25-'05</i>	<i>T. Hosen</i>	<i>T. Hosen</i>	DWG. NO.	MS5F6439	1 / 19
CHECKED	<i>Oct.-25-'05</i>	<i>T. Hosen</i>				
CHECKED	<i>Oct.-25-'05</i>	<i>T. Kokawa</i>				

Revised Records

Date	Classification	Index	Content	Drawn	Checked	Checked	Approved
Oct.-24 2005	enactment	—	—————	Y. Hara	T. HOSEN	T. Kobayashi	T. Miyajima

This material and the information herein is the property of Fuji Electric Device Technology Co., Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co., Ltd.

- 1.Scope** This specifies Fuji Power MOSFET FML19N50G
2.Construction N-Channel enhancement mode power MOSFET
3.Applications for Switching
4.Outview TFP Outview See to 8/19 page

5.Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain-Source Voltage	V_{DS}	500	V	
	V_{DSX}	500	V	VGS=-30V
Continuous Drain Current	I_D	± 19	A	
		± 1.4	A	Ta=25°C Note *1
Pulsed Drain Current	I_{DP}	± 76	A	
Gate-Source Voltage	V_{GS}	± 30	V	
Non-repetitive Maximum Avalanche Current	I_{AR}	19	A	Note *2
Non-Repetitive Maximum Avalanche Energy	E_{AS}	582.5	mJ	Note *3
Repetitive Maximum Avalanche Energy	E_{AR}	27.0	mJ	Note *4
Maximum Drain-Source dV/dt	dV_{DS}/dt	20	kV/ μ s	VDS \leq 500V
Peak Diode Recovery dV/dt	dV/dt	5	kV/ μ s	Note *5
Maximum Power Dissipation	P_D	270	W	Tc=25°C
		2.40		Ta=25°C Note *1
Operating and Storage Temperature range	T_{ch}	150	°C	
	T_{stg}	-55 to +150	°C	

6.Electrical Characteristics at Tc=25°C (unless otherwise specified)

Static Ratings

Description	Symbol	Conditions	min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu A$ $V_{GS}=0V$	500	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$I_D=250\mu A$ $V_{DS}=V_{GS}$	3.0	-	5.0	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=500V$ $V_{GS}=0V$ $T_{ch}=25^\circ C$	-	-	25	μA
		$V_{DS}=400V$ $V_{GS}=0V$ $T_{ch}=125^\circ C$	-	-	250	
Gate-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 30V$ $V_{DS}=0V$	-	-	100	nA
Drain-Source On-State Resistance	$R_{DS(on)}$	$I_D=9.5A$ $V_{GS}=10V$	-	0.29	0.38	Ω

This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

Dynamic Ratings

Description	Symbol	Conditions	min.	typ.	max.	Unit
Forward Transconductance	g_{fs}	$I_D=9.5A$ $V_{DS}=25V$	7.5	15	-	S
Input Capacitance	C_{iss}	$V_{DS}=25V$	-	1560	2340	pF
Output Capacitance	C_{oss}	$V_{GS}=0V$	-	230	345	
Reverse Transfer Capacitance	C_{rss}	$f=1MHz$	-	13	20	
Turn-On Time	$t_d(on)$	$V_{cc}=300V$	-	29	43.5	ns
	t_r	$V_{GS}=10V$	-	13	19.5	
Turn-Off Time	$t_d(off)$	$I_D=9.5A$	-	56	84	
	t_f	$R_{GS}=10\Omega$	-	8	12	
Total Gate Charge	Q_G	$V_{cc}=250V$	-	34	51	nC
Gate-Source Charge	Q_{GS}	$I_D=19A$	-	13	19.5	
Gate-Drain Charge	Q_{GD}	$V_{GS}=10V$	-	10	15	

Reverse Diode

Description	Symbol	Conditions	min.	typ.	max.	Unit
Diode Forward On-Voltage	V_{SD}	$I_F=19A$ $V_{GS}=0V$ $T_{ch}=25^\circ C$	-	1.20	1.50	V
Reverse Recovery Time	t_{rr}	$I_F=19A$ $V_{GS}=0V$	-	0.57	-	μs
Reverse Recovery Charge	Q_{rr}	$-di/dt=100A/\mu s$ $T_{ch}=25^\circ C$	-	7.0	-	μC

7. Thermal Resistance

Description	Symbol	min.	typ.	max.	Unit
Channel to Case	$R_{th}(ch-c)$			0.463	$^\circ C/W$
Channel to Ambient	$R_{th}(ch-a)$			87	$^\circ C/W$
Channel to Ambient	$R_{th}(ch-a)$ Note *1			52	$^\circ C/W$

Note *1 : Surface mounted on 1000mm²,t=1.6mm FR-4 PCB (Drain pad area : 500mm²)

Note *2 : $T_{ch} \leq 150^\circ C$, See Fig.1 and Fig.2

Note *3 : Starting $T_{ch}=25^\circ C$, $I_{AS}=8.0A$, $L=16.7mH$, $V_{cc}=50V$, $R_G=50\Omega$, See Fig.1 and Fig.2

E_{AS} limited by maximum channel temperature and avalanche current.

See to the 'Maximum Avalanche Energy' graph of page 18/19.

Note *4 : Repetitive rating : Pulse width limited by maximum channel temperature.

See to the 'Maximum Transient Thermal impedance' graph of page 19/19.

Note *5 : $I_F \leq -I_D$, $-di/dt=50A/\mu s$, $V_{cc} \leq BV_{DSS}$, $T_{ch} \leq 150^\circ C$

This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

Fig.1 Test circuit

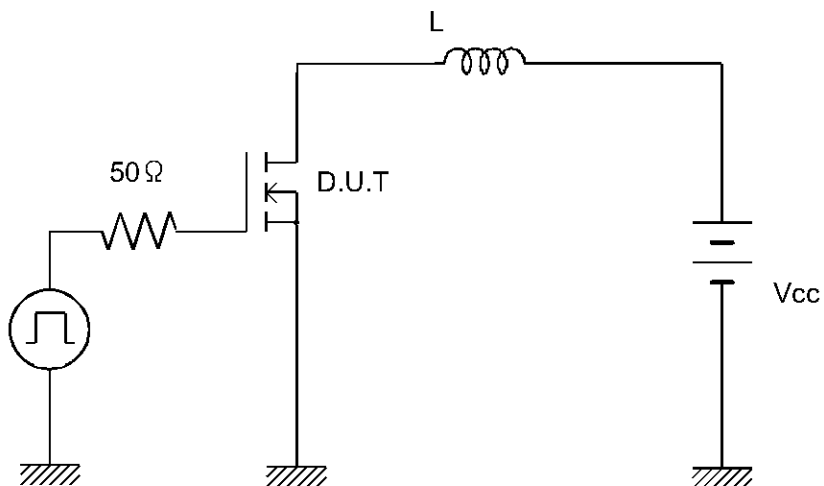
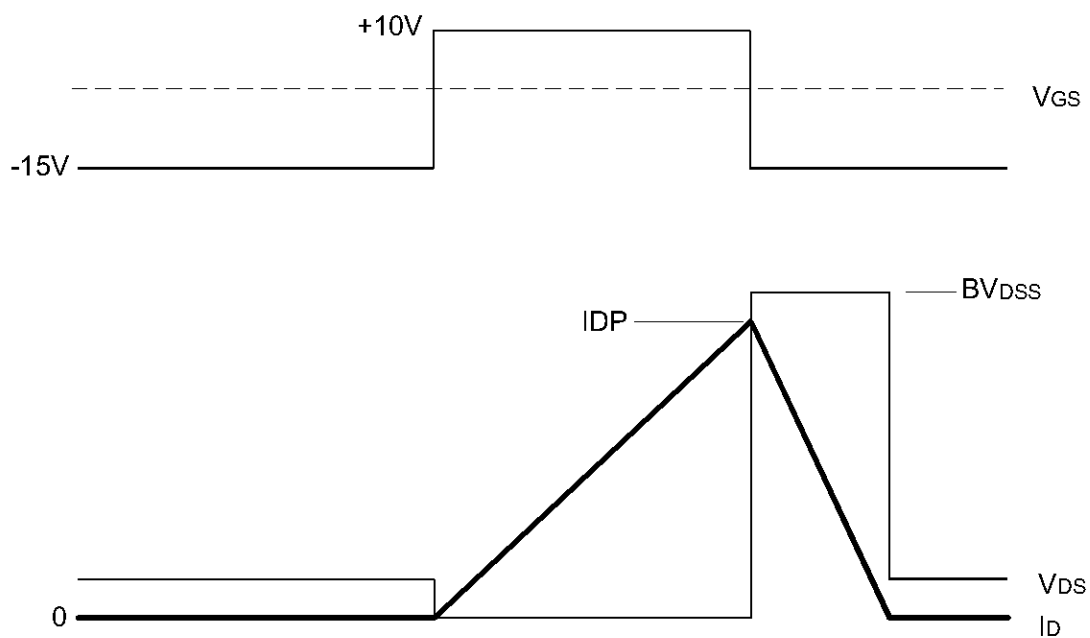


Fig.2 Operating waveforms



8. Reliability test items

All guaranteed values are under the categories of reliability per non-assembled(only MOSFETs). Each categories under the guaranteed reliability conform to EIAJ ED4701/100 method104 standards.

(Test items required without fail.
Humidification treatment (85±2°C,65±5%RH,168±24hr)
Heat treatment of soldering (IR-ray Reflow ,255±5°C(260°Cmax.),10±1sec,2 times)

	Test No.	Test Items	Testing methods and Conditions	Reference Standard	Sampling number	Acceptance number
Mechanical test methods	1	Vibration	frequency : 100Hz to 2kHz Acceleration : 200m/s ² Sweeping time : 4min. 48min. for each X,Y&Z directions.	EIAJ ED4701/400 method 403	15	(0:1)
	2	Shock	Peak amplitude: 15km/s ² Duration time : 0.5ms 3times for each X,Y&Z directions.	EIAJ ED4701/400 metod 404	15	
	3	Solderability	Solder temp. : 245±5°C Immersion time : 5±0.5sec	No conforming standard	15	
	4	Resistance to Soldering Heat	Solder temp. : 255±5°C Immersion time : 10±1sec Number of times : 2times IR-ray Reflowing	No conforming standard	15	

This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

	Test No.	Test Items	Testing methods and Conditions	Reference Standard	Sampling number	Acceptance number
Climatic test methods	1	High Temp. Storage	Temperature : 150+0/-5°C Test duration : 1000hr	EIAJ ED4701/200 method 201	22	(0:1)
	2	Low Temp. Storage	Temperature : -55+5/-0°C Test duration : 1000hr	EIAJ ED4701/200 method 202	22	
	3	Temperature Humidity Storage	Temperature : 85±2°C Relative humidity : 85±5% Test duration : 1000hr	EIAJ ED4701/100 method 103	22	
	4	Temperature Humidity BIAS	Temperature : 85±2°C Relative humidity : 85±5% Bias Voltage : $V_{DS(max)} * 0.8$ Test duration : 1000hr	EIAJ ED4701/100 method 103	22	
	5	Unsaturated Pressurized Vapor	Temperature : 130±2°C Relative humidity : 85±5% Vapor pressure : 230kPa Test duration : 48hr	EIAJ ED4701/100 method 103	22	
	6	Temperature Cycle	High temp.side : 150±5°C/30min. Low temp.side : -55±5°C/30min. RT : 5°C ~ 35°C/5min. Number of cycles : 100cycles	EIAJ ED4701/100 method 105	22	
	7	Thermal Shock	Fluid : pure water(running water) High temp.side : 100+0/-5°C Low temp.side : 0+5/-0°C Duration time : HT 5min,LT 5min Number of cycles : 100cycles	EIAJ ED4701/300 method 307	22	
Endurance test methods	8	Intermittent Operating Life	$\Delta T_c=90$ degree $T_{ch} \leq T_{ch(max)}$ Test duration : 3000 cycle	EIAJ ED4701/100 method 106	22	(0:1)
	9	HTRB (Gate-source)	Temperature : $T_{ch}=150+0/-5^\circ C$ Bias Voltage : $+V_{GS(max)}$ Test duration : 1000hr	EIAJ ED4701/100 method 101	22	
	10	HTRB (Drain-Source)	Temperature : $T_{ch}=150+0/-5^\circ C$ Bias Voltage : $V_{DS(max)}$ Test duration : 1000hr	EIAJ ED4701/100 method 101	22	

Failure Criteria

	Item	Symbols	Failure Criteria		Unit
			Lower Limit	Upper Limit	
Electrical Characteristics	Breakdown Voltage	BVDSS	LSL	----	V
	Zero gate Voltage Drain-Source Current	IDSS	----	USL	A
	Gate-Source Leakage Current	IGSS	----	USL	A
	Gate Threshold Voltage	VGS(th)	LSL	USL	V
	Drain-Source on-state Resistance	RDS(on)	----	USL	Ω
	Forward Transconductance	gfs	LSL	----	S
	Diode forward on-Voltage	VSD	----	USL	V
Outview	Marking Soldering and other damages	----	With eyes or Microscope		----

* LSL : Lower Specification Limit

* USL : Upper Specification Limit

* Before any of electrical characteristics measure, all testing related to the humidity have conducted after drying the package surface for more than an hour at 150°C.

Fuji Electric Device Technology Co.,Ltd.

DWG.NO.

MS5F6439

7/19

FUJI POWER MOS FET
TYPE : FML

OUT VIEW
外形寸法図

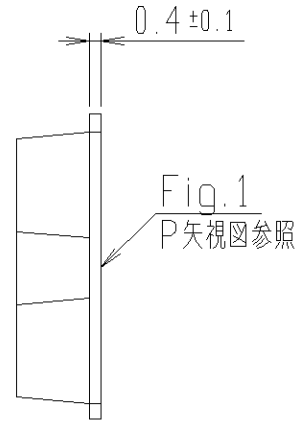
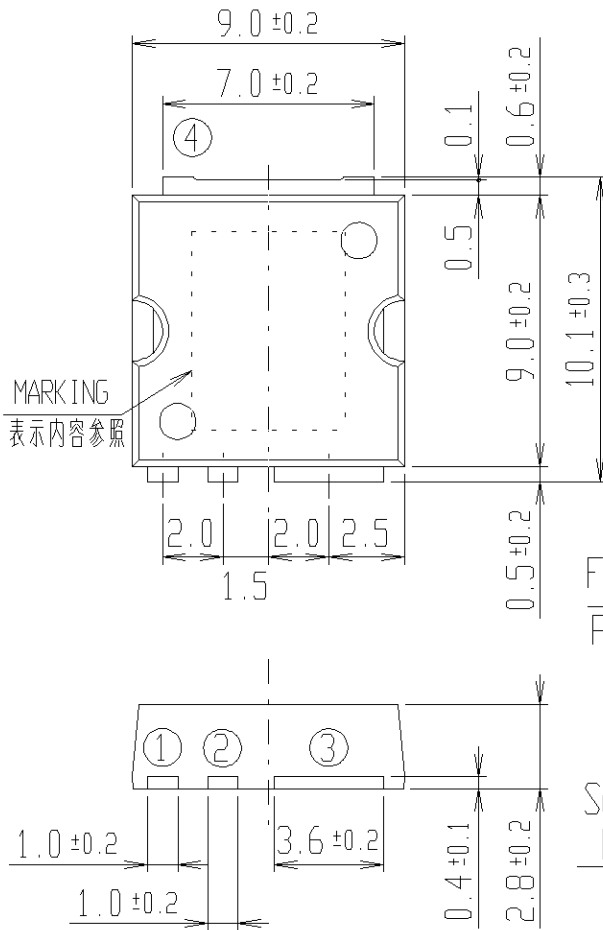
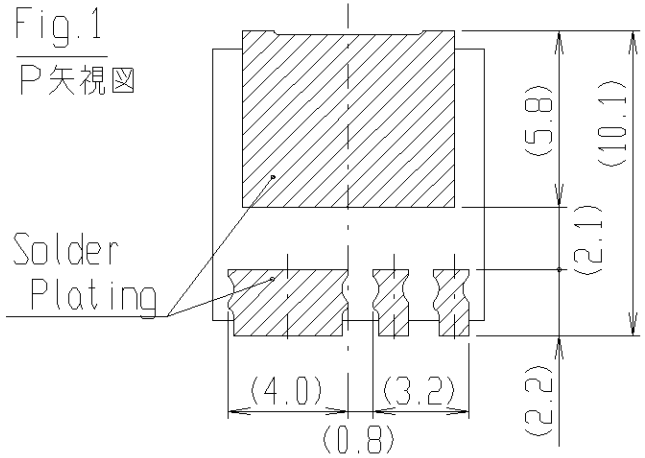


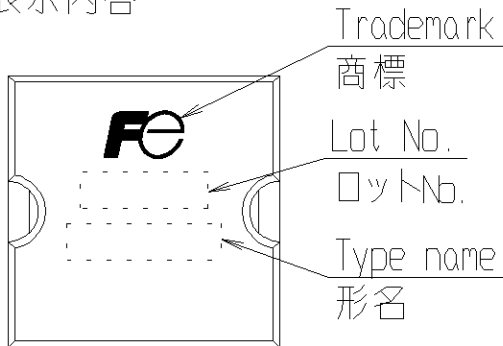
Fig. 1
P 矢視図



MARKING
表示内容参照

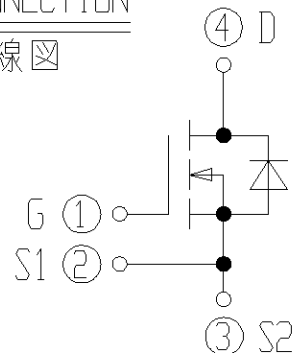
DIMENSIONS ARE IN MILLIMETERS.

MARKING
表示内容



Note: 1. Dimension shown in () is reference values.
注) 1. () 内寸法は参考値とする。

CONNECTION
結線図



This material and the information herein is the property of Fuji Electric Device Technology Co., Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co., Ltd.

9. Cautions

- Although Fuji Electric Device Technology is continually improving product quality and reliability, a small percentage of semiconductor products may become faulty. When using Fuji Electric Device Technology semiconductor products in your equipment, you are requested to take adequate safety measures to prevent the equipment from causing physical injury, fire, or other problem in case any of the products fail. It is recommended to make your design fail-safe, flame retardant, and free of malfunction.
- The products described in this Specification are intended for use in the following electronic and electrical equipment which has normal reliability requirements.
 - Computers · OA equipment · Communications equipment(Terminal devices)
 - Machine tools · AV equipment · Measurement equipment
 - Personal equipment · Industrial robots · Electrical home appliances etc.
- The products described in this Specification are not designed or manufactured to be used in equipment or systems used under life-threatening situations. If you are considering using these products in the equipment listed below, first check the system construction and required reliability, and take adequate safety measures such as a backup system to prevent the equipment from malfunctioning.
 - Backbone network equipment · Transportation equipment (automobiles, trains, ships, etc.)
 - Traffic-signal control equipment · Gas alarms, leakage gas auto breakers
 - Submarine repeater equipment · Burglar alarms, fire alarms, emergency equipment
 - Medical equipment · Nuclear control equipment etc.
- Do not use the products in this Specification for equipment requiring strict reliability such as (but not limited to):
 - Aerospace equipment · Aeronautical equipment

10. Warnings

- The MOSFETs should be used in products within their absolute maximum rating (voltage, current, temperature, etc.).
- The MOSFETs may be destroyed if used beyond the rating.
- We only guarantee the non-repetitive and repetitive Avalanche capability and not for the continuous Avalanche capability which can be assumed as abnormal condition. Please note the device may be destructed from the Avalanche over the specified maximum rating.
- The equipment containing MOSFETs should have adequate fuses or circuit breakers to prevent the equipment from causing secondary destruction (ex. fire, explosion etc...).
- Use the MOSFETs within their reliability and lifetime under certain environments or conditions. The MOSFETs may fail before the target lifetime of your products if used under certain reliability conditions.
- Be careful when handling MOSFETs for ESD damage. (It is an important consideration.)
- When handling MOSFETs, hold them by the case (package) and don't touch the leads and terminals.
- It is recommended that any handling of MOSFETs is done on grounded electrically conductive floor and tablemats.

- Before touching a MOSFET terminal, Discharge any static electricity from your body and clothes by grounding out through a high impedance resistor (about 1M Ω)
- When soldering, in order to protect the MOSFETs from static electricity, ground the soldering iron or soldering bath through a low impedance resistor.
- You must design the MOSFETs to be operated within the specified maximum ratings(voltage, current, temperature, etc.) to prevent possible failure or destruction of devices.
- Consider the possible temperature rise not only for the channel and case, but also for the outer leads.
- Do not directly touch the leads or package of the MOSFETs while power is supplied or during operation in order to avoid electric shock and burns.
- The MOSFETs are made of incombustible material. However, if a MOSFET fails, it may emit smoke or flame. Also, operating the MOSFETs near any flammable place or material may cause the MOSFETs to emit smoke or flame in case the MOSFETs become even hotter during operation. Design the arrangement to prevent the spread of fire.
- The MOSFETs should not used in an environment in the presence of acid, organic matter, or corrosive gas(hydrogen sulfide, sulfurous acid gas etc.)
- The MOSFETs should not used in an irradiated environment since they are not radiation-proof.

Installation

- Soldering involves temperatures which exceed the device storage temperature rating. To avoid device damage and to ensure reliability, observe the following guidelines from the quality assurance standard.

Soldering methods

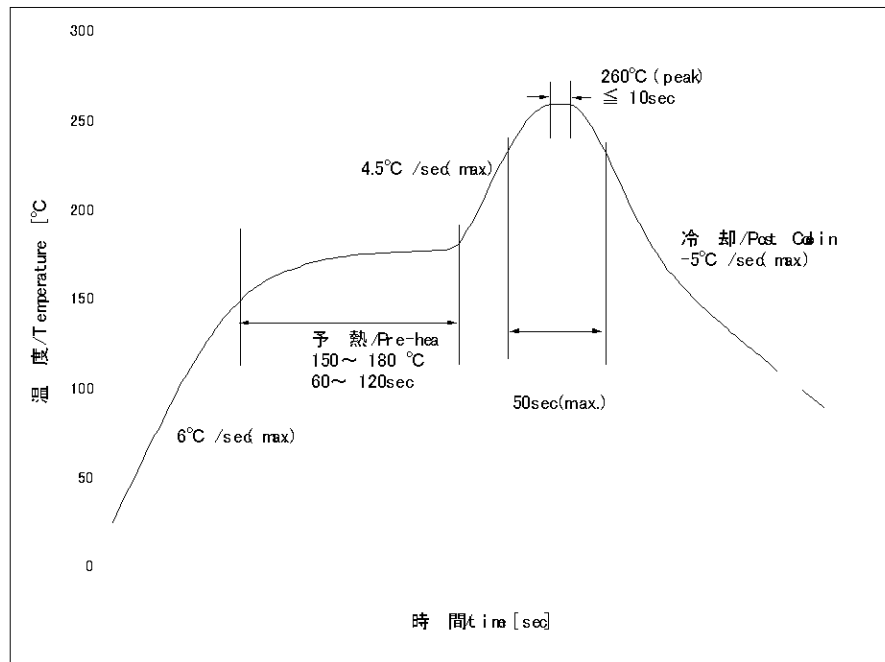
Packages	Methods				
	Wave Soldering (Full dipping)	Wave Soldering (Only terminal)	Infrared Reflow	Air Reflow	Soldering iron (Re-work)
T-Pack(S)	×	×	◎	◎	×
T-Pack(SJ)	×	×	◎	◎	×
K-Pack(S)	×	×	◎	◎	×
TFP	×	×	◎	◎	×

◎:Possible ○:Limited to 1 time ×:Unable

Soldering temperature and duration

	Reflow-Soldering
Number of times	Twice
Soldering Temp. & Time	≥230°C, ≤50sec
Package surface Peak Temp. & Time	≤260°C, ≤10sec

Recommended Reflow profile



Storage

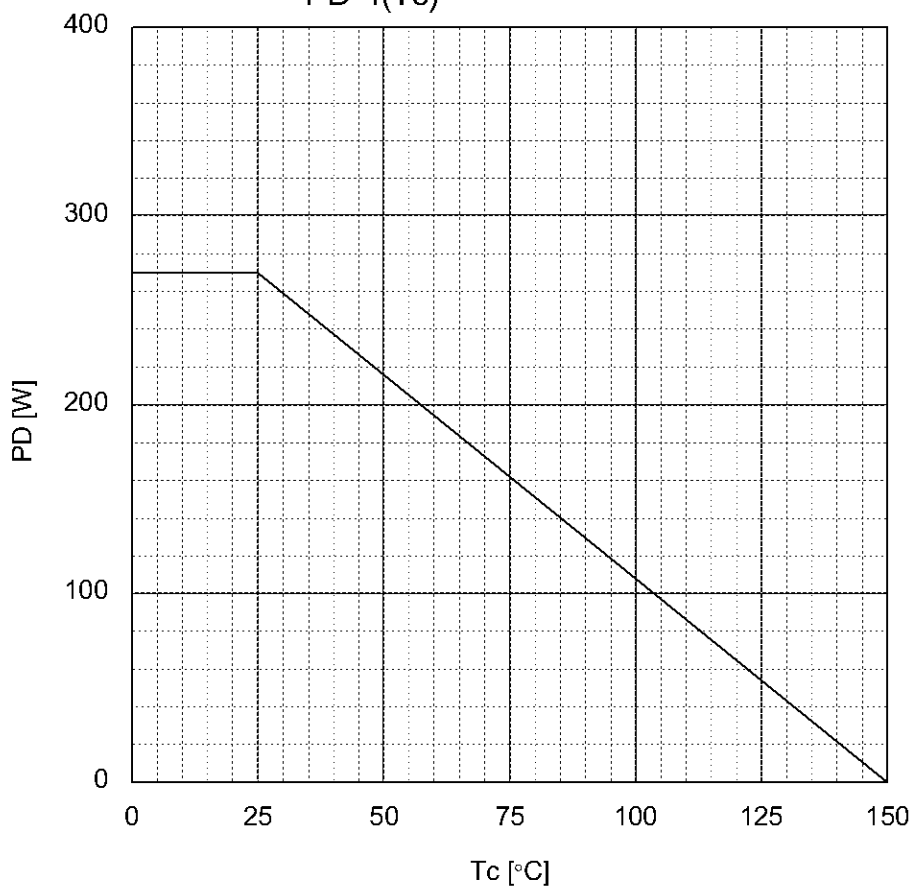
- The MOSFETs must be stored at a standard temperature of 5 to 35 °C and relative humidity of 45% to 75%.
- If the storage area is very dry, a humidifier may be required. In such a case, use only deionized water or boiled water, since the chlorine in tap water may corrode the leads.
- The MOSFETs should not be subjected to rapid changes in temperature to avoid condensation on the surface of the MOSFETs. Therefore store the MOSFETs in a place where the temperature is steady.
- The MOSFETs should not be stored on top of each other, since this may cause excessive external force on the case.
- The MOSFETs should be stored with the lead terminals remaining unprocessed. Rust may cause presoldered connections to fail during later processing.
- The MOSFETs should be stored in antistatic containers or shipping bags.

11. Appendix

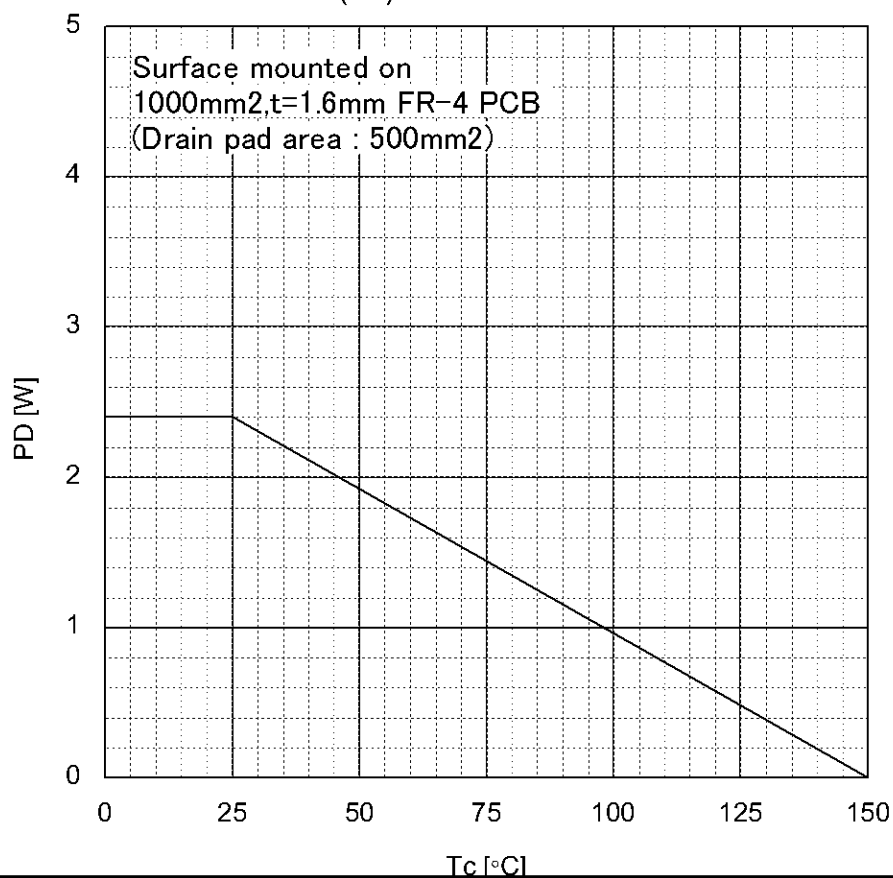
- These products do not contain PBBs (Polybrominated Biphenyl) or PBDEs (Polybrominated Diphenyl Ether).
- These products do not contain Class-I ODS and Class-II ODS of 'Clean Air Act of US'.

- If you have any questions about any part of this Specification, please contact Fuji Electric Device Technology or its sales agent before using the product.
- Neither Fuji nor its agents shall be held liable for any injury caused by using the products not in accordance with the instructions.
- The application examples described in this specification are merely typical uses of Fuji Electric Device Technology products.
- This specification does not confer any industrial property rights or other rights, nor constitute a license for such rights.

Allowable Power Dissipation
 $PD=f(T_c)$



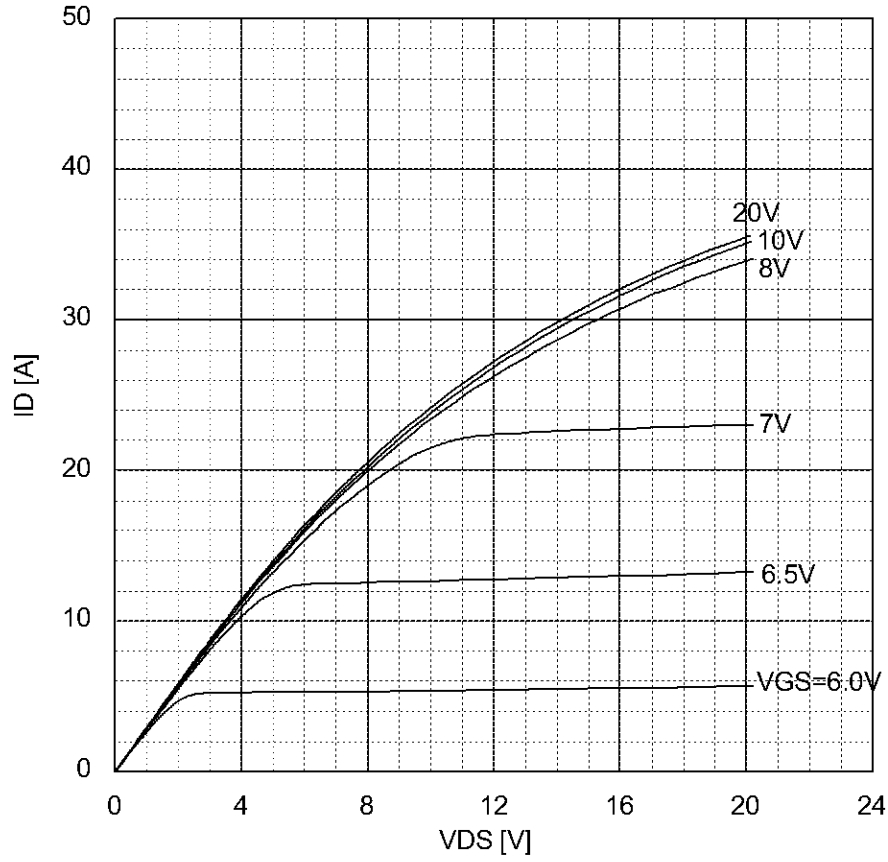
Allowable Power Dissipation
 $PD=f(T_c)$



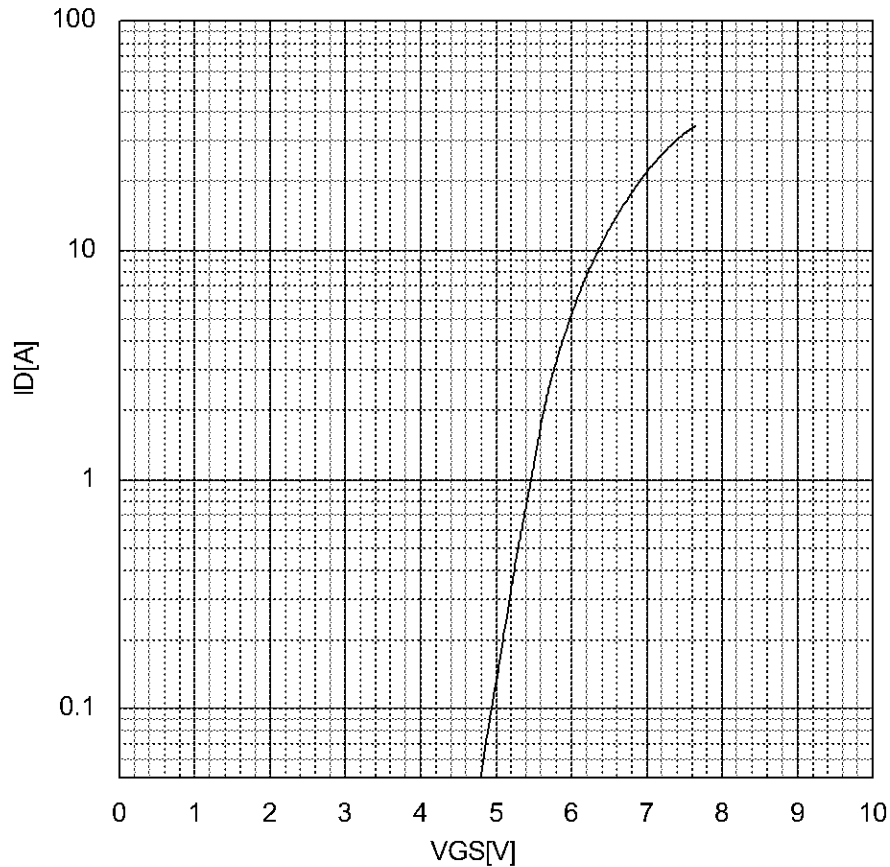
This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

Typical Output Characteristics
 $I_D=f(V_{DS}):80\ \mu\text{s pulse test}, T_{ch}=25\ ^\circ\text{C}$

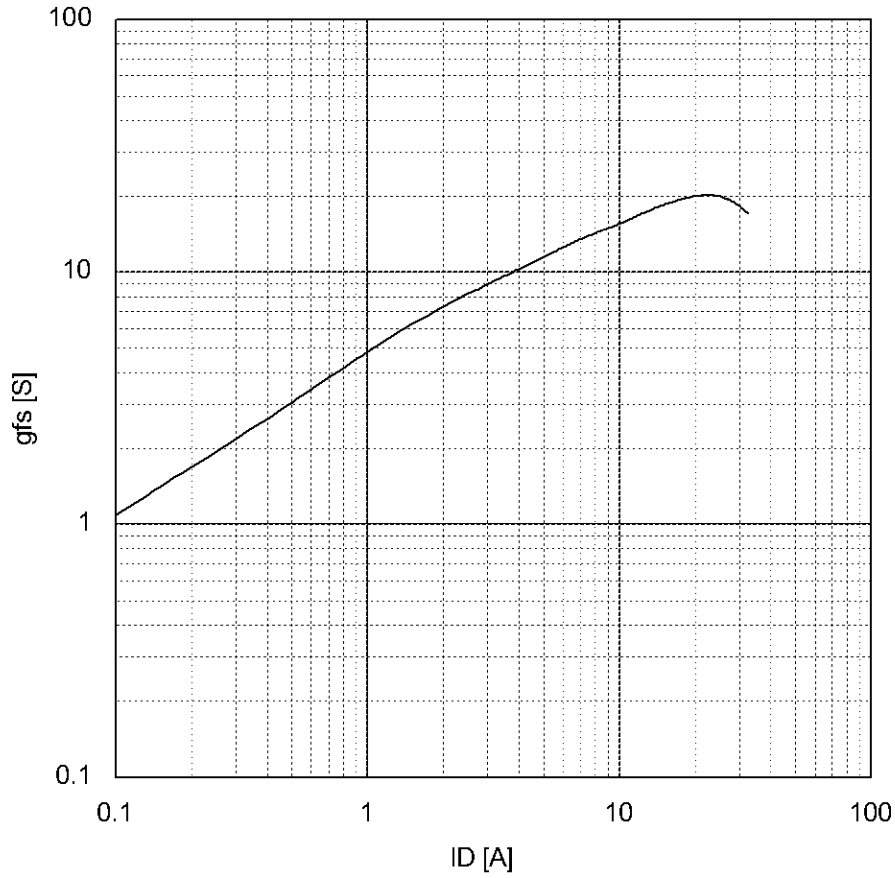


Typical Transfer Characteristic
 $I_D=f(V_{GS}):80\ \mu\text{s pulse test}, V_{DS}=25\text{V}, T_{ch}=25\ ^\circ\text{C}$

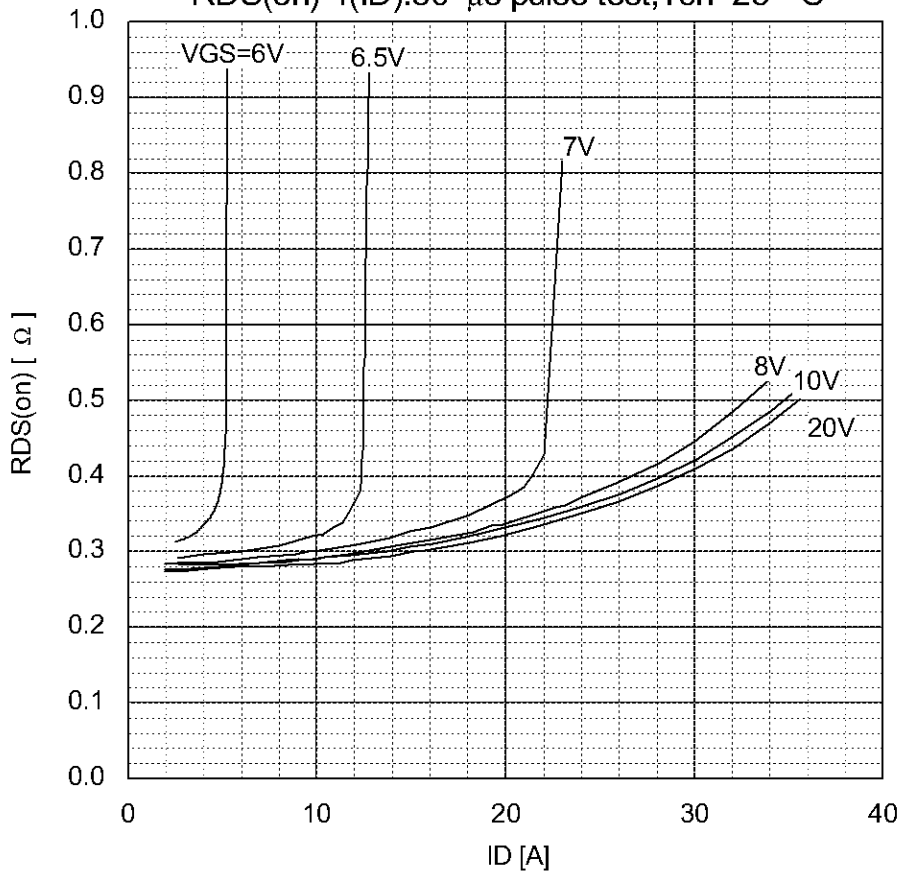


This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

Typical Transconductance
 $g_{fs}=f(I_D):80\ \mu\text{s pulse test}, V_{DS}=25\text{V}, T_{ch}=25\ \text{°C}$

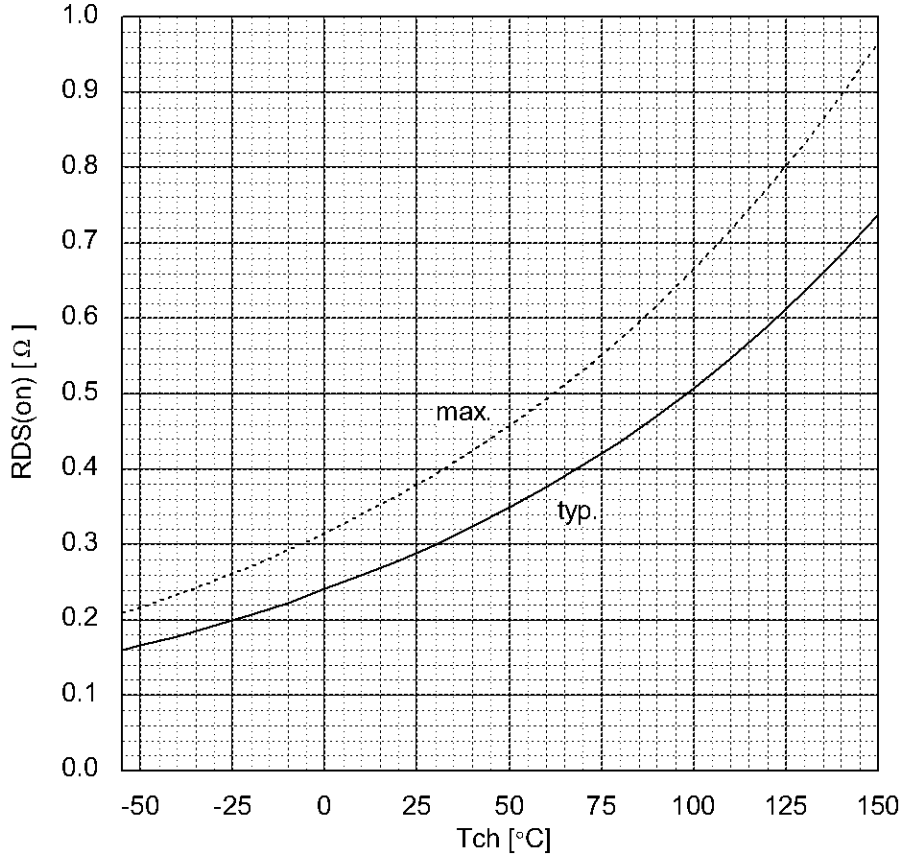


Typical Drain-Source on-state Resistance
 $R_{DS(on)}=f(I_D):80\ \mu\text{s pulse test}, T_{ch}=25\ \text{°C}$

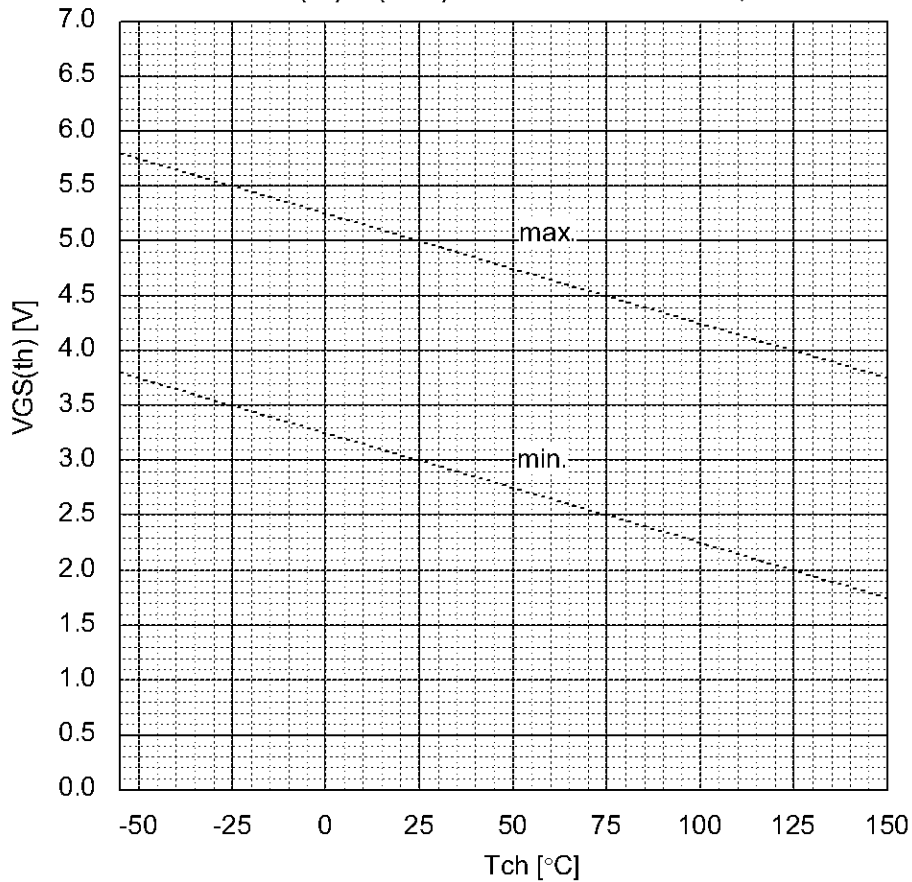


This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

Drain-Source On-state Resistance
 $R_{DS(on)}=f(T_{ch}):I_D=9.5A,V_{GS}=10V$

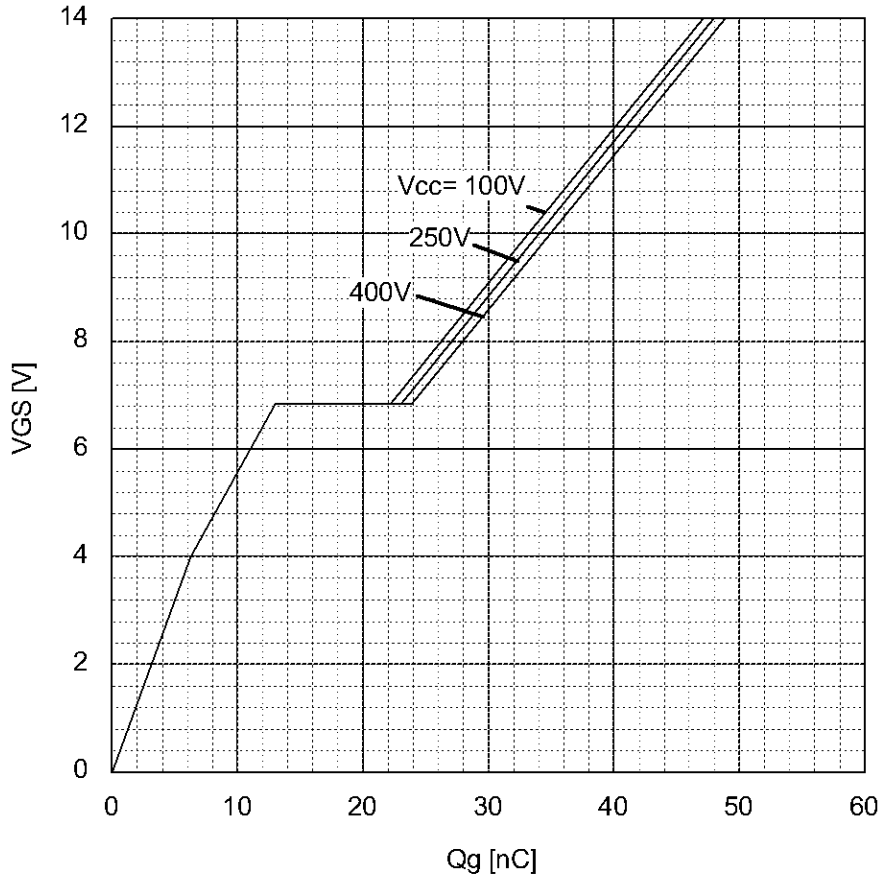


Gate Threshold Voltage vs. Tch
 $V_{GS(th)}=f(T_{ch}):V_{DS}=V_{GS},I_D=250\mu A$

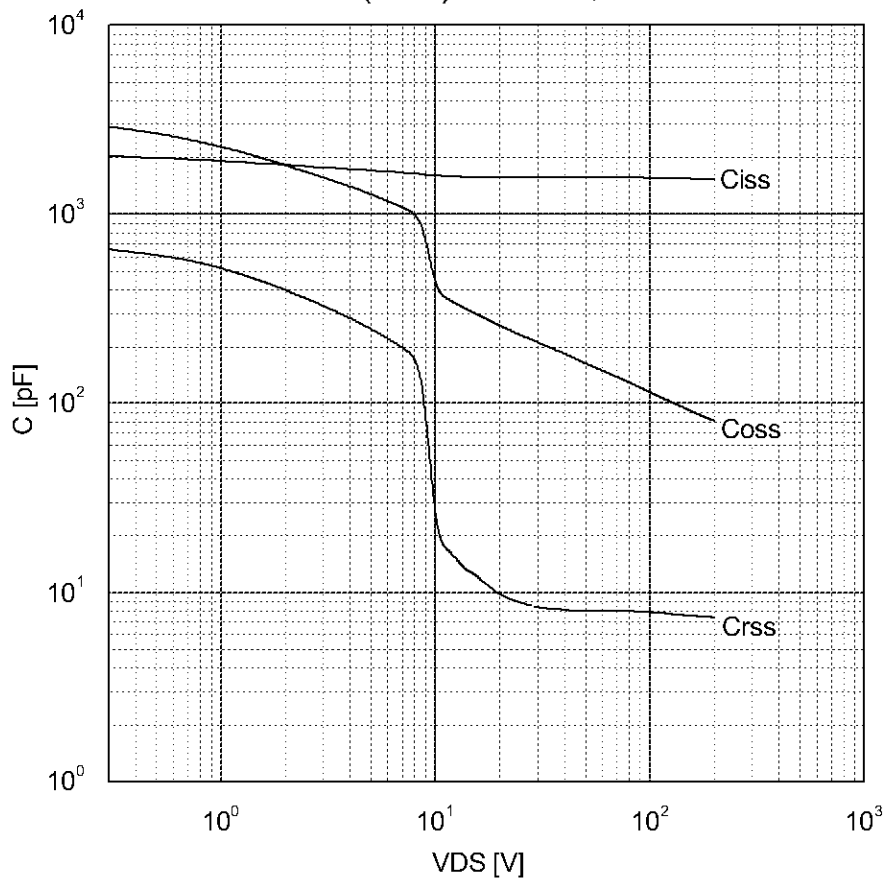


This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

Typical Gate Charge Characteristics $V_{GS}=f(Q_g): I_D=19A, T_{ch}=25^\circ C$

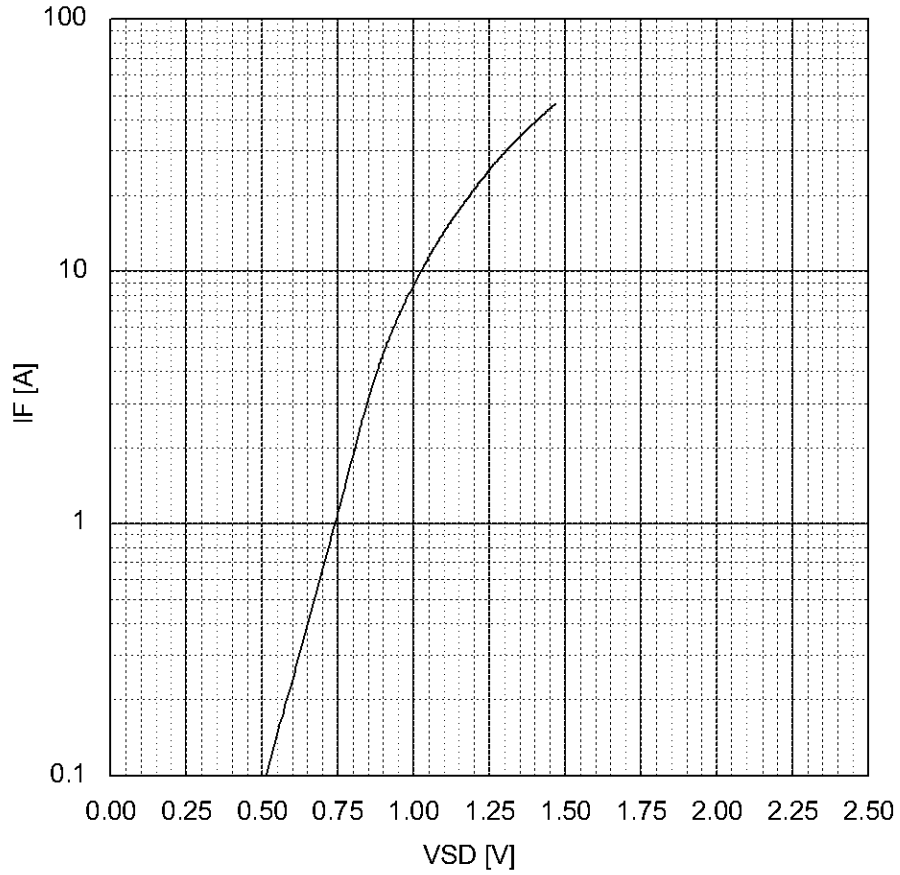


Typical Capacitance $C=f(V_{DS}): V_{GS}=0V, f=1MHz$

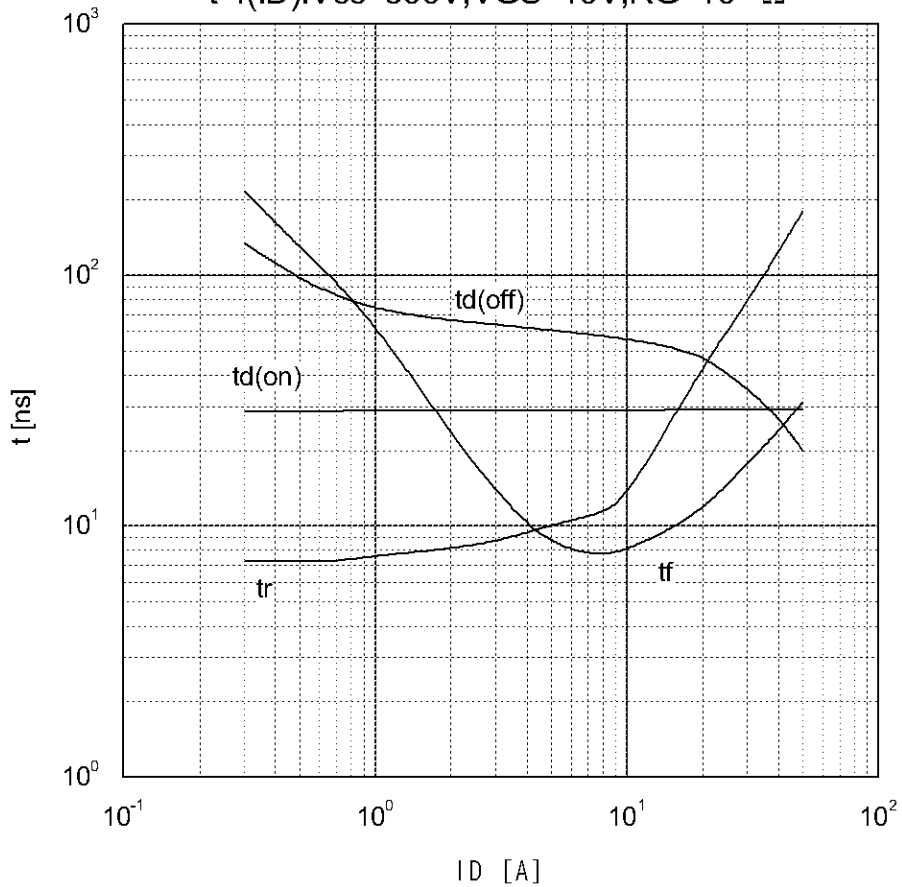


This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

Typical Forward Characteristics of Reverse Diode
 $I_F=f(V_{SD}): 80 \mu s$ pulse test, $T_{ch}=25 \text{ }^\circ\text{C}$



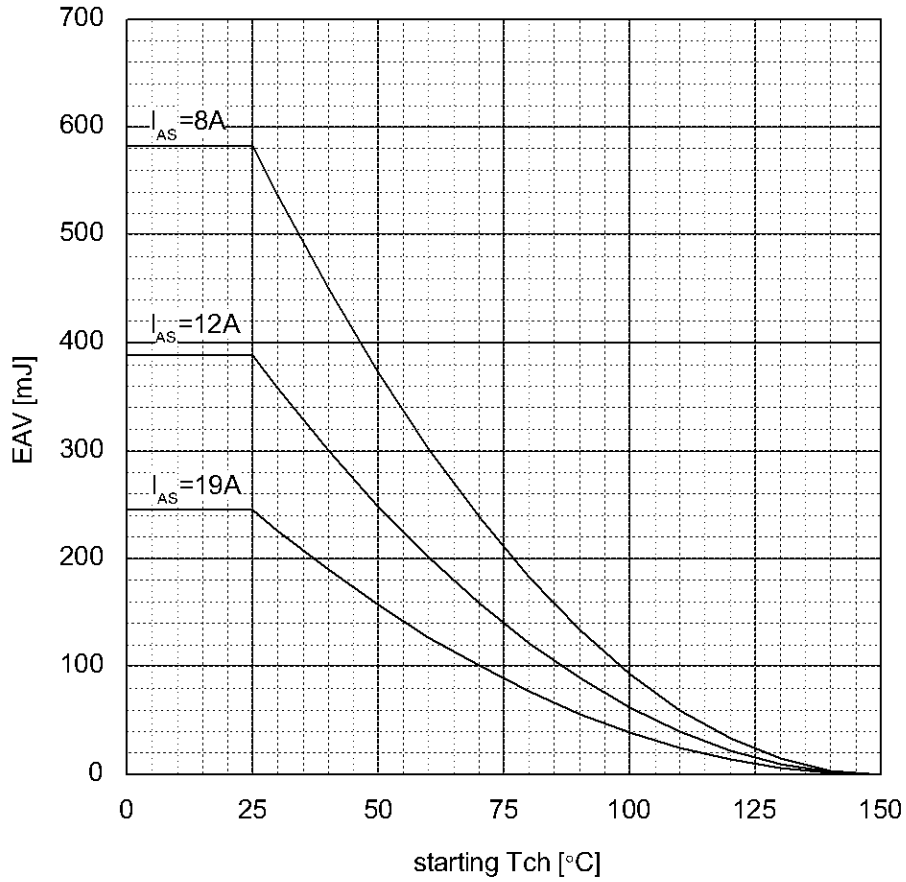
Typical Switching Characteristics vs. I_D
 $t=f(I_D): V_{cc}=300V, V_{GS}=10V, R_G=10 \ \Omega$



This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

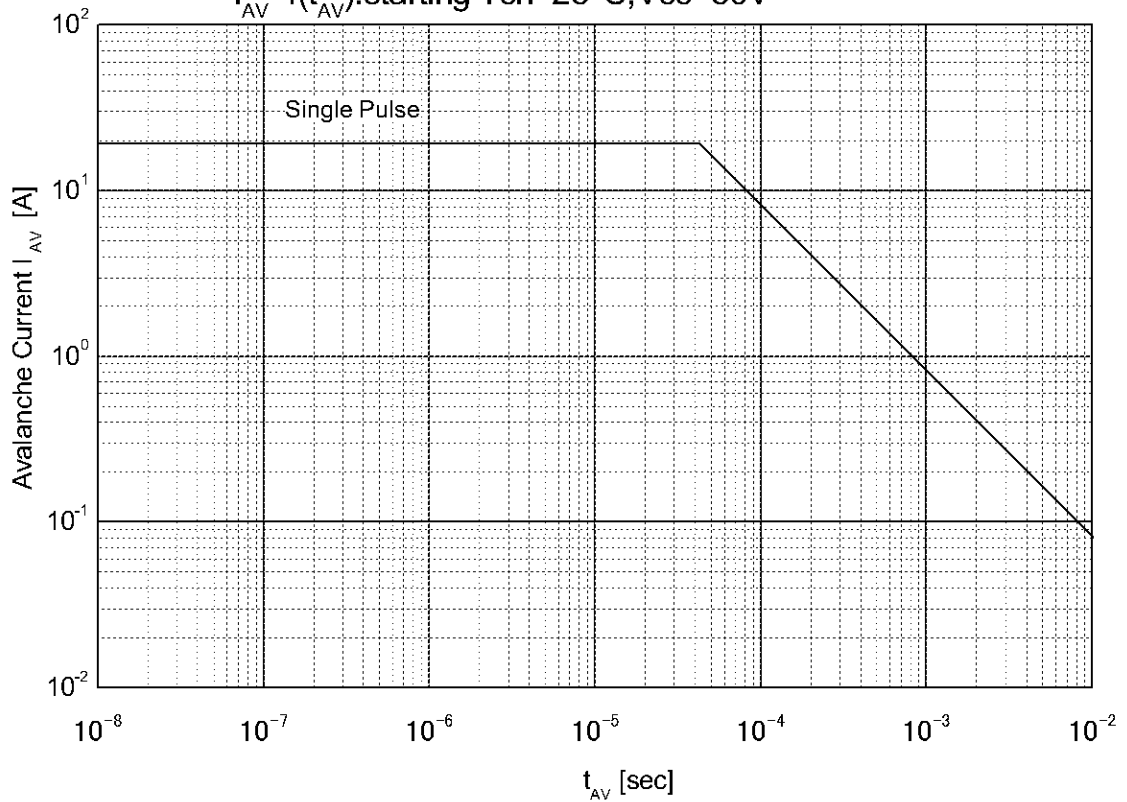
Maximum Avalanche Energy vs. starting Tch

$E(AV)=f(\text{starting Tch}); V_{CC}=50V$



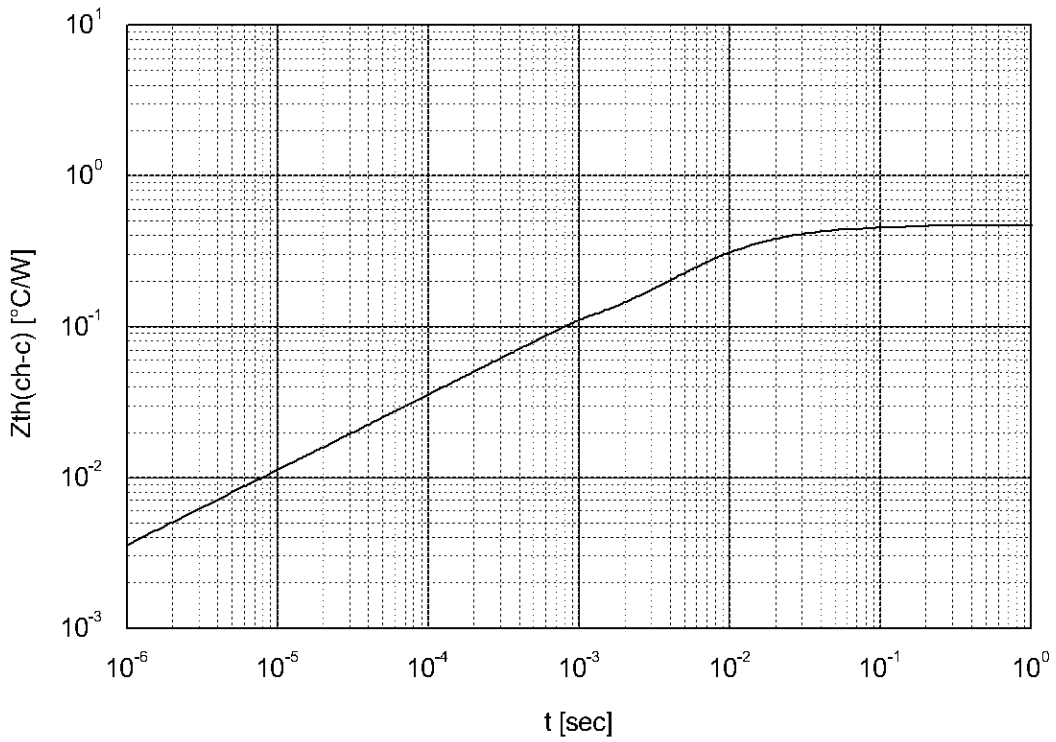
Maximum Avalanche Current Pulsewidth

$I_{AV}=f(t_{AV}); \text{starting Tch}=25^{\circ}C, V_{CC}=50V$



This material and the information herein is the property of Fuji Electric Device Technology Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Device Technology Co.,Ltd.

Maximum Transient Thermal Impedance
 $Z_{th}(ch-c)=f(t):D=0$



Thermal Resistance vs. Drain Pad area
 $t=1.6\text{mm FR-4 PCB}$

